

## Final Exam, ECE 137A

**Wednesday March 22, 2006     8-11 AM**

Name: \_\_\_\_\_

Closed Book Exam: Class Crib-Sheet and 3 pages (6 surfaces) of student notes permitted  
Do not open this exam until instructed to do so. Use any and all reasonable approximations (5% accuracy), *after stating & justifying them.*

**Show your work:**

**Full credit will not be given for correct answers if supporting work is missing.**

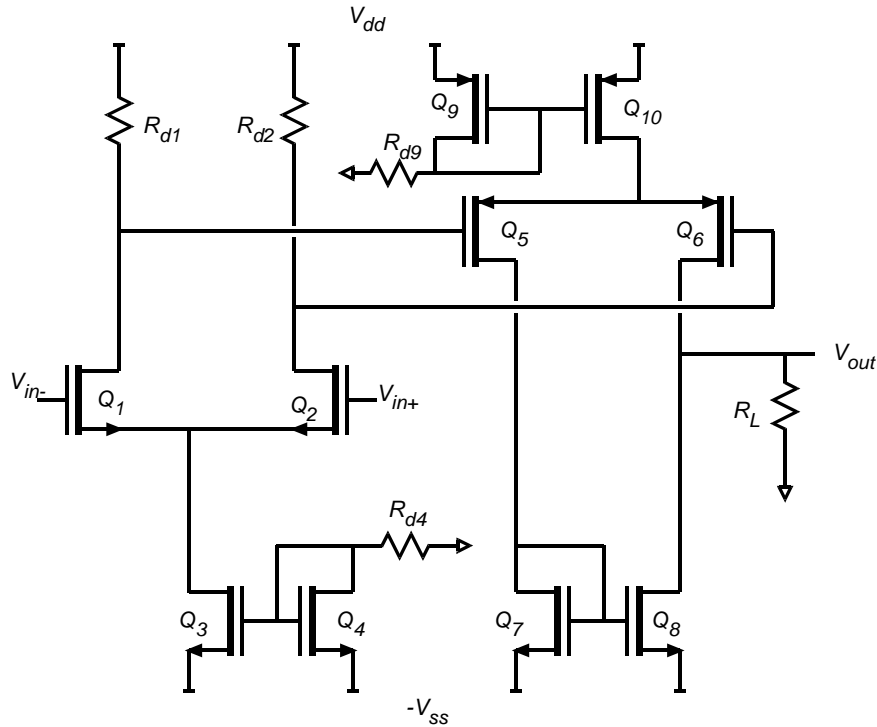
Good luck

| Time function                        | LaPlace Transform                                |
|--------------------------------------|--|
| $\delta(t)$                          | 1  |
| $U(t)$                               | $1/s$  |
| $e^{-\alpha t}U(t)$                  | $\frac{1}{s + \alpha}$                           |
| $e^{-\alpha t} \cos(\omega_d t)U(t)$ | $\frac{s + \alpha}{(s + \alpha)^2 + \omega_d^2}$ |
| $e^{-\alpha t} \sin(\omega_d t)U(t)$ | $\frac{\omega_d}{(s + \alpha)^2 + \omega_d^2}$   |

| Part  | Points Received | Points Possible | Part | Points Received | Points Possible |
|-------|-----------------|-----------------|------|-----------------|-----------------|
| 1a    |                 | 5               | 2b   |                 | 10              |
| 1b    |                 | 5               | 2c   |                 | 15              |
| 1c    |                 | 5               | 3a   |                 | 7               |
| 1d    |                 | 10              | 3b   |                 | 8               |
| 1e    |                 | 10              | 3c   |                 | 7               |
| 2a    |                 | 10              | 3d   |                 | 8               |
| TOTAL |                 | 100             |      |                 |                 |

**Problem 1, 35 points**

(This *NOT* an Op-Amp)



N-channel devices:  $V_{th} = 0.5$  Volts.  $v_{sat} C_{ox} W_g = 0.5$  mA/V,  $\lambda = 1/(20$  Volts)

P-channel devices:  $|V_{th}| = 0.5$  Volts.  $v_{sat} C_{ox} W_g = 0.5$  mA/V,  $\lambda = 1/(20$  Volts)

$V_{dd} = +5$  volts.  $-V_{ss} = -5$  volts

$R_L = 100$  kOhm.

Input DC bias condition:  $V_{in+} = V_{in-} = \text{zero volts}$

Part a, 5 points

DC bias. *Ignore the  $\lambda$  factor during DC bias calculations*

The DC output voltage is to be zero volts if  $V_{in+}=V_{in-} = \text{zero volts}$

Q1, Q5, Q5, and Q6 are all to be biased at 0.2 mA drain current.

The gates of Q5 and Q6 are to be biased at +3.5 Volts

Find:  $R_{d1} = \underline{\hspace{2cm}}$        $R_{d2} = \underline{\hspace{2cm}}$

$R_{d4} = \underline{\hspace{2cm}}$        $R_{d9} = \underline{\hspace{2cm}}$





Part c. 5 points

Find the small signal parameters below

| FETs | $g_m$ | $R_{ds}$ |
|------|-------|----------|
| Q1   |       |          |
| Q2   |       |          |
| Q3   |       |          |
| Q5   |       |          |
| Q6   |       |          |
| Q7   |       |          |
| Q8   |       |          |
| Q9   |       |          |
| Q10  |       |          |

Part d, 10 points.

Given these definitions, find the following

|                         | Gain |
|-------------------------|------|
| Differential Pair Q1/Q2 |      |
| Differential Pair Q5/Q6 |      |
| Overall Vout/Vin        |      |





Part e, 10 points

Maximum peak-peak output voltage (*show all your work*)

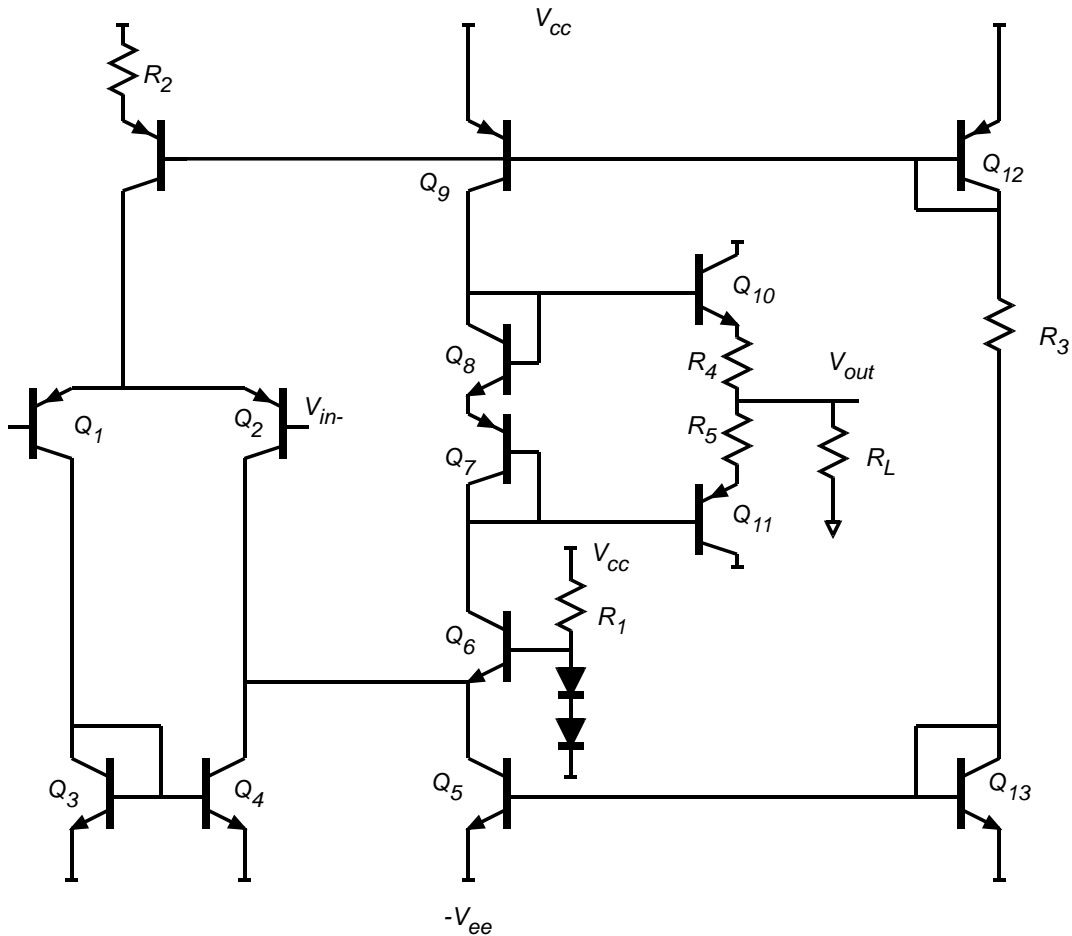
|               | magnitude and sign of maximum output signal swing due to <i>cutoff</i> | magnitude and sign of maximum output signal swing due to:<br><i>knee voltage</i> (FETs) |
|---------------|--|---|
| Transistor Q5 |  | -----N/A-----   |
| Transistor Q6 |  |   |
| Transistor Q8 |  |   |



**Problem 2, 35 points**

This is an Op-Amp

Analyze under the assumption that the DC output is at zero volts



NPNs:  $\beta = 300$ ,  $V_A = 100$  volts, all NPNs have equal  $I_s$ .

PNPs:  $\beta = 300$ ,  $V_A = 100$  volts, all PNPs have equal  $I_s$ .

Except: the output transistors (Q10, Q11) have  $I_s$  10:1 greater than all other transistors.

$V_{cc} = +10$  volts,  $-V_{ss} = -10$  volts,  $R_L = 10 \text{ k}\Omega$

Part a, 10 points

DC bias.

Q9 is to be biased at 2 mA collector current.

Q10 is to be biased at 2 mA collector current.

Q1 and Q2 are to be biased at 0.2 mA collector current.

Find:

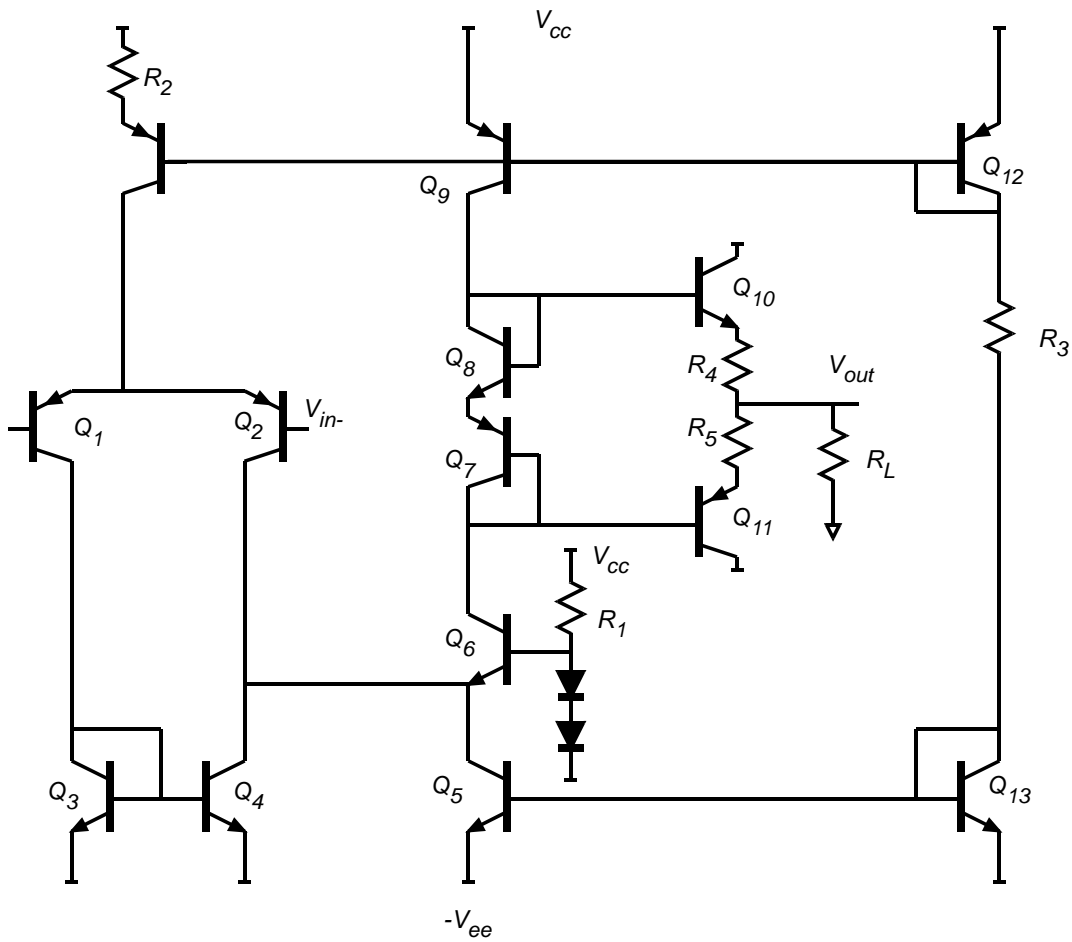
R1=\_\_\_\_\_ R2=\_\_\_\_\_

R3=\_\_\_\_\_ R4=\_\_\_\_\_ R5=\_\_\_\_\_



Part b, 10 points

DC bias



On the circuit diagram above, label the DC voltages at **ALL nodes** and the drain currents of **ALL transistors**

Part c, 15 points.

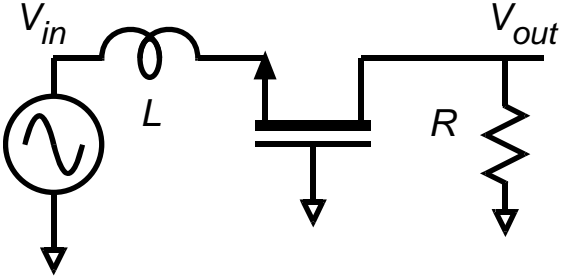
Find the following

|  | Voltage Gain | Input impedance |
|--|--------------|-----------------|
| Transistor combination<br>Q1, Q2, Q3, Q4 |              |                 |
| Transistor Q6                            |              |                 |
| Push-pull state Q10/Q11                  |              |                 |
| Overall differential<br>$V_{out}/V_{in}$ |              |                 |





**Problem 3, 30 points**

|   |  |
|---|--|
|  | <p>You will be working on the circuit to the left</p> <p>Ignore DC bias analysis. You don't need it.</p> <p>The transistor has transconductance <math>g_m</math>. Its output resistance <math>R_{ds}</math> is infinity.</p> |
|---|--|

Part a, 7 points

Draw a small-signal equivalent circuit of the circuit.

Part b, 8 points

$g_m=10 \text{ mS}$ .  $L=10 \text{ nH}$ .  $R= 1000 \text{ Ohms}$

Find, by nodal analysis, a small-signal expression for  $V_{out}/V_{in}$

$V_{out}(s)/V_{in}(s)=$  \_\_\_\_\_

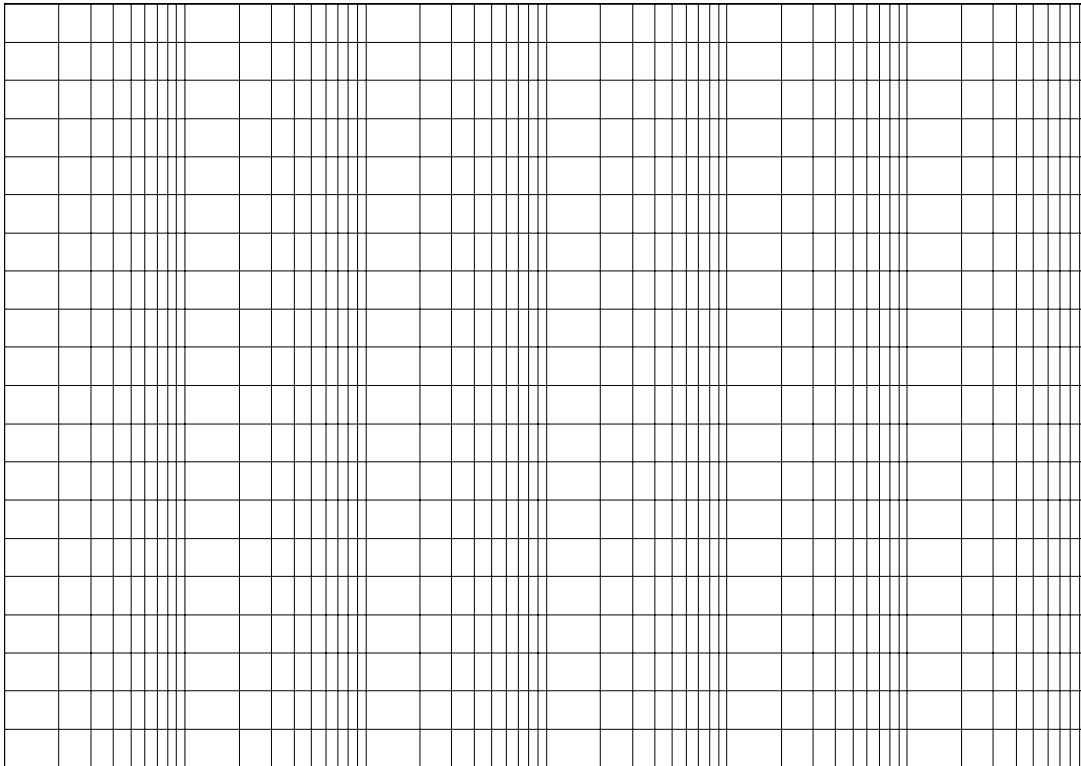


Part c, 7 points

Find any/all pole and zero frequencies of the transfer function, in Hz:

\_\_\_\_\_ , \_\_\_\_\_ , \_\_\_\_\_ , \_\_\_\_\_

Draw a clean Bode Plot of  $V_{out}/V_{in}$ ,  
LABEL AXES, LABEL all relevant gains and pole or zero frequencies, Label Slopes





Part d, 8 points

$V_{in}(t)$  is a 0.5 V amplitude step-function.

Find  $V_{out}(t) = \underline{\hspace{10cm}}$

Plot it below. Label axes, show initial and final values, show time constants.

