Department of Electrical & Computer Engineering
University of California, Santa Barbara

ECE 2B
Summer 2009
Shynk
H.O. #3

COURSE OBJECTIVES AND TENTATIVE SCHEDULE

Objectives: Continuation of introductory circuit analysis. Diode circuits. Laplace transform and solution of steady-state and transient circuit problems in the s-domain; Bode plots; resonators; op-amps and design of op-amp circuits; passive and active filters; Fourier series and Fourier transformers. Two-port circuit parameters and their use in small signal transistor circuit analysis.

Dates:

Week 1 (6/22):	Labs Begin	
Week 2 (6/29):	Homework #1 Due (Thursday 7/2)	Lab #1 Due (Wednesday 7/1)
Week 3 (7/6):	Homework #2 Due (Friday 7/10)	
Week 4 (7/13):	Homework #3 Due (Friday 7/17)	Lab #2 Due (Wednesday 7/15)
Week 5 (7/20):	Homework #4 Due (Friday 7/24)	Lab #3 Due (Wednesday 7/22)
Week 6 (7/27):	Midterm Exam (Thursday, 7/30)	Lab #4 Due (Wednesday 7/29)
Week 7 (8/3):	Homework #5 Due (Friday 8/7)	Lab #5 Due (Wednesday 8/5)
Week 8 (8/10):	Homework #6 Due (Friday 8/14)	Lab #6 Due (Wednesday 8/12)
Week 9 (8/17):	Homework #7 Due (Friday 8/21)	Lab #7 Due (Wednesday 8/19)
Week 10 (8/24):	Final Exam (Thursday, 8/27)	

Notes:

1. Homework will be due on Fridays at 5:00 p.m. (except for Homework #1, which is due on Thursday because of the holiday). Place them in the ECE 2B homework box in Room 3120 of Harold Frank Hall (HFH).

2. Lab reports will be due on Wednesdays at 5:00 p.m. Place them in the ECE 2B homework box in HFH 3120.

3. You should attend the lab session on Tuesday. The TA office hours for homework problems will be held during the Thursday lab session; you may also continue working on the labs on that day if you need additional time.