

Following is a brief introduction to how to use Quartus.

1. First create the project (file- create project wizard)
2. Project settings that you need to choose are given on the website <http://vader.ece.ucsb.edu/digilab-fpga/>
3. Name of the module should be the same as the name of the project which should be the same as the name of the verilog file.  
Verilog file name = name of project = name of the module
4. After creation of the project, add the verilog file to the project
5. Right – click on the file added and choose "Set – as top level entry"
6. Assignment -> Device -> Device&Pinout. Do the configurations, which are given on the website <http://vader.ece.ucsb.edu/digilab-fpga/>
7. Processing -> start compilation
8. After compilation is successful, go to Assignments -> assignment Editor (pinout assignments)
9. Select the "Pins" button and if you have done everything right you should see the inputs and outputs of your module
10. Just assign them to any of the general purpose pins on the FPGA board.
11. Save the assignment and compile again.
12. After compilation is successful download the .rbf file to the FPGA board using the FPGATool on the website.