

Department of Electrical & Computer Engineering
University of California, Santa Barbara

ECE 152A
Summer 2011
Shynk
H.O. #1

ECE 152A COURSE INFORMATION

Title: Digital Design Principles

Lectures: Monday and Wednesday, 3:30–4:50 p.m., WEBB 1100

Instructor: Professor John J. Shynk (shynk@ece.ucsb.edu)

Office: HFH 3123, 893-3977

Office Hours: Monday and Wednesday, 5:00–6:00 p.m.

Teaching Assistants: Amir Aminzadeh-Gohari (amirazg@ece.ucsb.edu)

Melika Ebrahimian-Payvand (melika01@umail.ucsb.edu)

Wei Tang (w_tang@umail.ucsb.edu)

Labs: Tuesday, 2:00–4:50 p.m., HFH 1124 (Amir)

Tuesday, 5:00–7:50 p.m., HFH 1124 (Melika)

Wednesday, 6:00–8:50 p.m., HFH 1124 (Wei)

Grading: Homework (weekly) \approx 15%

Lab (weekly) \approx 20%

Midterm Exam (open book) \approx 30% (Wednesday, July 27, 3:30–4:30 p.m.)

Final Exam (open book) \approx 35% (Wednesday, August 24, 3:30–4:50 p.m.)

Required Textbook: Brown & Vranesic, *Fundamental Digital Logic with Verilog Design*, McGraw-Hill, 2nd Edition, 2008

Course Web Site: http://www.ece.ucsb.edu/courses/ECE152/152A_Su11Shynk/