ECE 225
High-Speed Digital Integrated Circuit Design

University of California, Santa Barbara
Department of Electrical and Computer Engineering
Winter 2015

Course Focus:

- Nanometer scale issues in VLSI: Devices, Technologies, Circuits & CAD
- Emerging Technologies for VLSI

Class Room/Schedule: Eng 2, Room 3301, Tue & Thu 4:00PM-5:50PM

Instructor: Prof. Kaustav Banerjee, Room 4151, Harold Frank Hall
Email: kaustav@ece.ucsb.edu
URL: http://nrl.ece.ucsb.edu/
Office Hours: Fri 1:00-2:00PM or appointment by email.

Text: The following books are recommended:

References: To be posted on class website: http://tinyurl.com/ECE225S14

Software: MMI: Max and Sue VLSI design tools, Avanti HSPICE, Mentor Graphics: XCalibre, and Model Sim Suites (available via ECI account. Software manuals available on course website)

Prerequisites: ECE124A or equivalent is recommended

Grading: Assignments - 30%, Final Exam - 40%, Final project - 30% (must be attempted individually)

Note: Must complete all assignments and final project to pass.
Projects can be selected from a broad range of topics to be discussed in class.