

**ECE ECE145A (undergrad) and ECE218A (graduate)
Final Exam. Monday December 6, 2021, noon - 3 p.m.**

Open book. You have 3 hrs.

Use all reasonable approximations (5% accuracy is fine.),

AFTER STATING and justifying THEM.

Think before doing complex calculations. Sometimes there is an easier way.

Problem	Points Received	Points Possible
1A		5
1B		5
1C		5
1D		5
1D		5
1F		5
1G		10 (218A only)
2		10
3		10
4A		10
4B		10 (218A only)
5A		5
5B		5
total		(145A), 114 (218A)

$$G_T = \frac{|S_{21}|^2 (1 - |\Gamma_s|^2)(1 - |\Gamma_L|^2)}{|(1 - \Gamma_s S_{11})(1 - \Gamma_L S_{22}) - S_{21} S_{12} \Gamma_s \Gamma_L|^2} \quad G_P = \frac{1}{1 - \|\Gamma_{in}\|^2} \cdot |S_{21}|^2 \cdot \frac{1 - |\Gamma_L|^2}{|1 - \Gamma_L S_{22}|^2}$$

$$G_a = \frac{1 - |\Gamma_s|^2}{|1 - \Gamma_s S_{11}|^2} \cdot |S_{21}|^2 \cdot \frac{1}{1 - \|\Gamma_{out}\|^2} \quad G_{max} = \frac{|S_{21}|}{|S_{12}|} \cdot \left[K - \sqrt{K^2 - 1} \right] \text{ if } K > 1$$

$$G_{MS} = \frac{|S_{21}|}{|S_{12}|} \cdot \text{if } K < 1 \quad K = \frac{1 - |S_{11}|^2 - |S_{22}|^2 + |\Delta|^2}{2 |S_{21} S_{12}|} \quad \text{where } \Delta = \det[S]$$

Unconditionally stable if : (1) $K > 1$ and (2) $\|\det[S]\| < 1$

Problem 1, 30 points (145A), 40 points (218A)

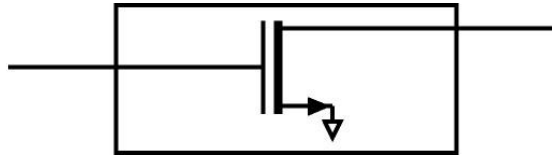
Power Gain Definitions

part a, 5 points

At 10 GHz, the transistor has

$$S_{11} = -0.5, S_{21} = +2, S_{12} = 0, S_{22} = +0.5$$

The generator has 100 Ohms source impedance and 1 mW available power. The load is 25 Ohms.



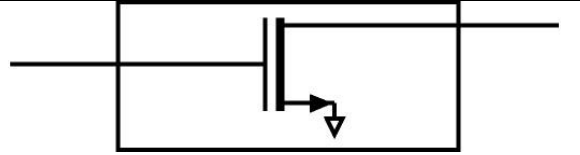
If we place impedance-matching networks between the generator and the transistor, and between the transistor and the load, what RF power will be delivered to the load ?

RF power delivered to the load = _____

part b. 5 points

At 10 GHz, the transistor has
 $S_{11} = -0.5$, $S_{21} = +2$, $S_{12} = 0$, $S_{22} = +0.5$

The generator has 50 Ohms source impedance and 1 mW available power. The load is 50 Ohms.



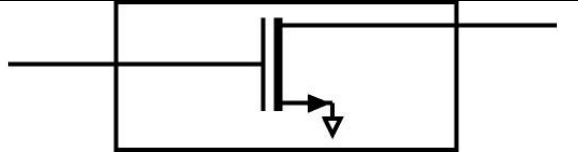
If we directly connect the generator and load to the transistor, what RF power will be delivered to the load ?

RF power delivered to the load = _____

part c, 5 points

At 10 GHz, the transistor has
 $S_{11} = -0.5$, $S_{21} = +2$, $S_{12} = 0$, $S_{22} = +0.5$

The generator has 100 Ohms source impedance and 1 mW available power. The load is 25 Ohms.



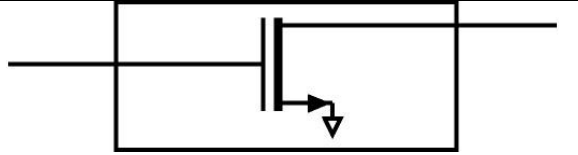
If we directly connect the generator and load to the transistor, what RF power will be delivered to the load ?

RF power delivered to the load = _____

part d, 5 points

At 10 GHz, the transistor has
 $S_{11} = -0.5$, $S_{21} = +2$, $S_{12} = 0$, $S_{22} = +0.5$

The generator has 100 Ohms source impedance and 1 mW available power. The load is 25 Ohms.



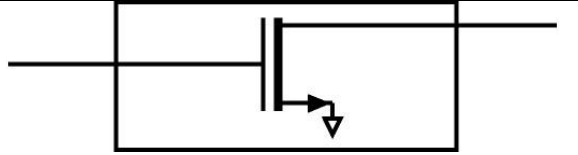
If we impedance-match the generator to the transistor input, but directly connect the load to the transistor output, what RF power will be delivered to the load ?

RF power delivered to the load = _____

part e, 5 points

At 10 GHz, the transistor has
 $S_{11} = -0.5$, $S_{21} = +2$, $S_{12} = 0$, $S_{22} = +0.5$

The generator has 100 Ohms source impedance and 1 mW available power. The load is 25 Ohms.



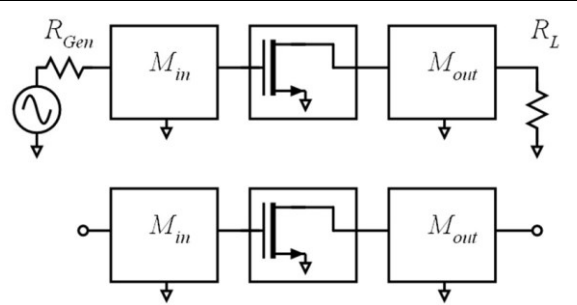
If we directly connect the generator to the transistor input, but impedance-match the load to the transistor output, what RF power will be delivered to the load ?

RF power delivered to the load = _____

part f, 5 points

At 10 GHz, the transistor has
 $S_{11} = -0.5$, $S_{21} = +2$, $S_{12} = 0$, $S_{22} = +0.5$

The generator has 25 Ohms source impedance and 1 mW available power. The load is 100 Ohms.



We first impedance-match the generator to the transistor input and then impedance-match the load to the transistor output (upper diagram). We then disconnect the generator and the load (lower diagram), leaving us with the transistor and its input and output networks, which we can an "amplifier".

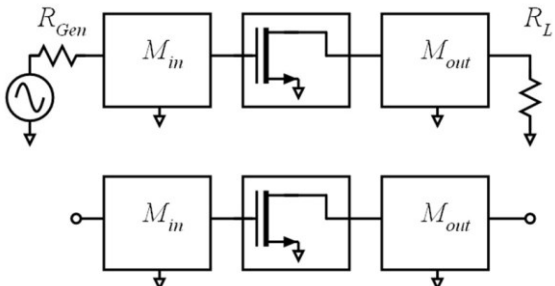
Please find the following:

S_{11} of the "amplifier"= _____

S_{22} of the "amplifier"= _____

$\|S_{21}\|$ of the "amplifier"= _____

part g. 10 points (218A only)

<p>At 10 GHz, the transistor has $S_{11} = -0.5$, $S_{21} = +2$, $S_{12} = 0$, $S_{22} = +0.5$</p> <p>The generator has 25 Ohms source impedance and 1 mW available power. The load is 100 Ohms.</p>	
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We first impedance-match the generator to the transistor input and then impedance-match the load to the transistor output (upper diagram). We then disconnect the generator and the load (lower diagram), leaving us with the transistor and its input and output networks, which we can an "amplifier".

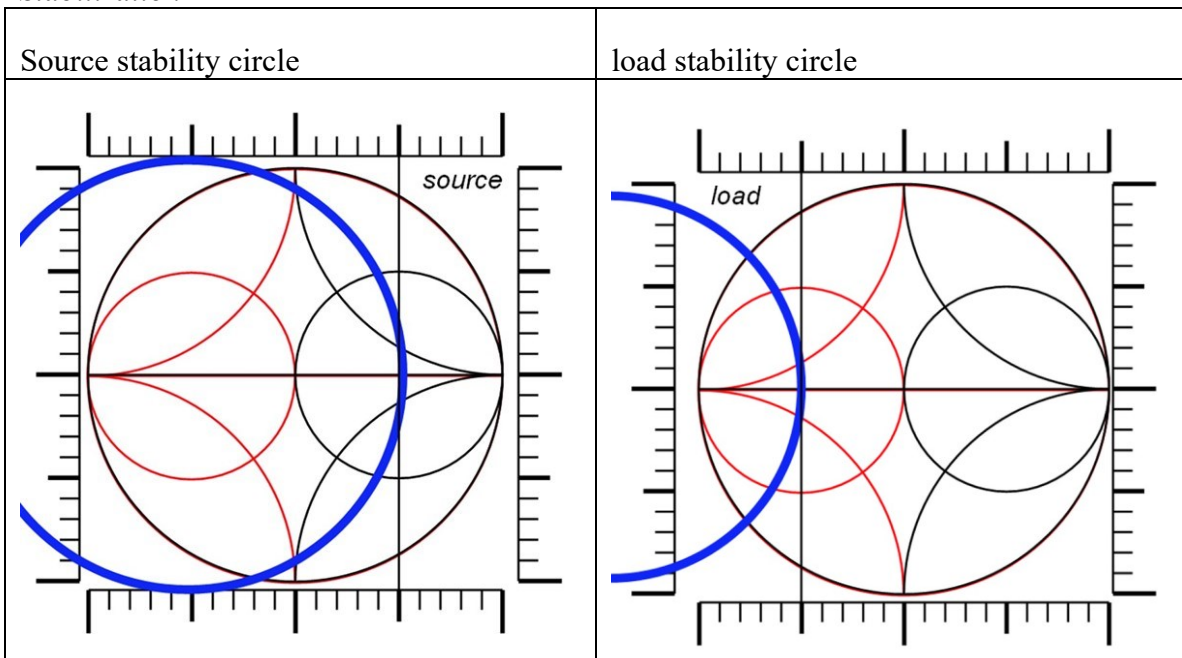
Please find the following:

$\|S_{21}\|$ of the "amplifier"= _____

****This will required some hard thinking****

Problem 2, 10 points

Stabilization



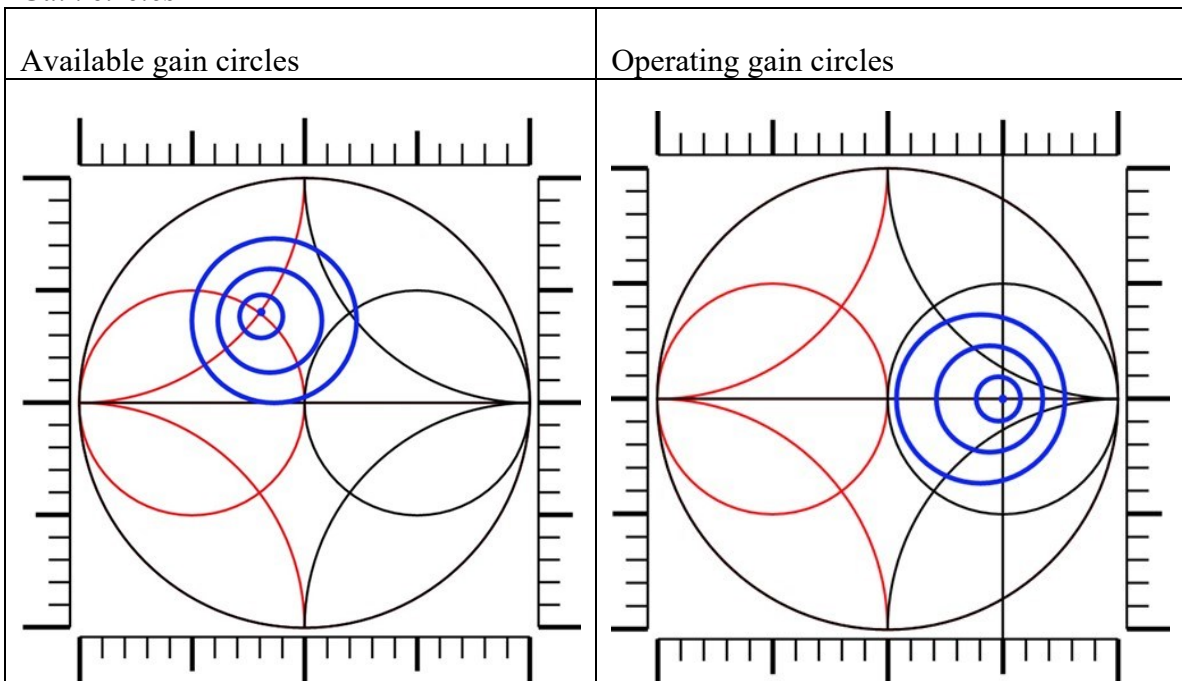
A MOSFET in common-source mode has $\|S_{11}\| < 1$ and $\|S_{22}\| > 1$. Source and load stability circles are as shown. The Smith charts use 50 Ohms impedance normalization. Draw ****2**** circuit diagrams, giving resistor values, of methods of stabilizing the transistor. ***Please draw your answers in the 2 boxes below***

circuit #1

circuit #2

Problem 3, 10 points

Gain circles



A FET in common-source mode has operating and available gain circles as shown (50 Ohm impedance normalization). Find the optimum generator and load impedances (in complex Ohms).

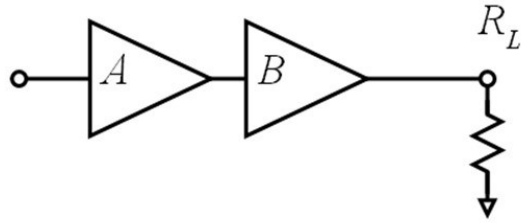
optimum source impedance= _____

optimum load impedance= _____

Problem 4, 10 points (145A), 20 points (218A)
2-port parameters and signal flow graphs

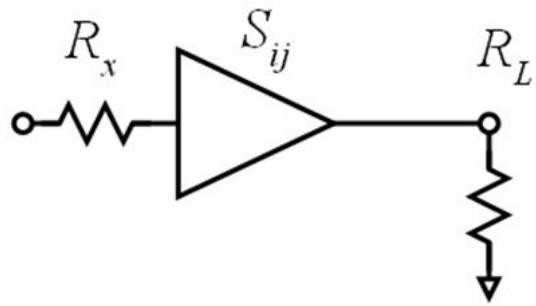
Part a, 10 points

Amplifiers A and B have S-parameters S_{ij}^A and S_{ij}^B . The output is connected to a load with reflection coefficient Γ_L . As a function of these given parameters, compute Γ_{in} , the input reflection coefficient.



Part b, 10 points (218a only)

The amplifier has (50 Ohm normalization)
 $S_{11} = -0.5$, $S_{21} = +2$, $S_{12} = 1/5$, $S_{22} = +0.5$.
The load is 100 Ohms, and a resistor,
 $R_x = 50$ Ohms, is connected to the input.
Using properties of S-parameters and
signal flow graphs, find Γ_{in} of the resulting
network



Problem 5, 10 points

Power amplifier design

part a, 5 points

You are working in some mm-wave CMOS technology. The maximum safe current is 1 mA per micrometer of gate width. For wide bandwidth (high f_{max}), the maximum gate width is 1.0 micrometers; set the gate width at this value, but use multiple gate fingers to further increase maximum gate current to some desired value.

The maximum safe drain-source voltage is 1.2V, and the minimum (knee) voltage is 0.2 Volts.

What is the maximum RF power per 1 micron gate finger ? _____

If the minimum impedance we could tune to were 10 Ohms, how many parallel gate fingers would we use in the power transistor, so that the required load impedance were 10 Ohms? _____

(please round the answer to the nearest integer)

What output power would that cell produce ? _____

What would be the drain efficiency ? _____

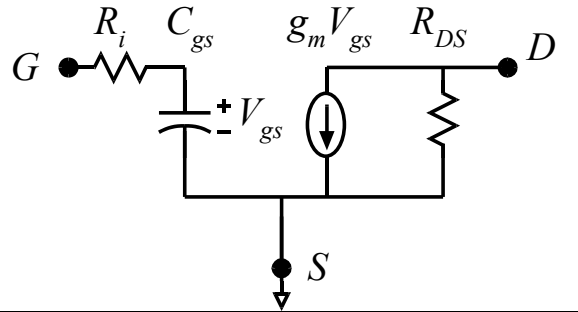
part c, 5 points

The transistor is now modelled by the equivalent circuit to the right.

$$g_m = 2 \text{ mS} * (\text{number gate fingers})$$

$$f_\tau = 100 \text{ GHz}$$

$$R_{ds} = \text{infinity}, R_i = 1/g_m \text{ Ohms,}$$



Given a 10 Ohm load, and the number of gate fingers you have found earlier, what input power at 10 GHz is necessary to produce this maximum output power ?

