

ECE ECE145A (undergrad) and ECE218A (graduate)
TAKE HOME Final Exam. Return before Friday December 15, 2017

HONOR SYSTEM: Open book. You have 3 hrs. WORK WITHOUT HELP.

Use all reasonable approximations (5% accuracy is fine.),

AFTER STATING and justifying THEM.

Think before doing complex calculations. Sometimes there is an easier way.

Problem	Points Received	Points Possible
1A		5
1B		5
1C		5
1D		10
1E		10
1F		10 (218A only)
2A		10
2B		5
2C		5
2D		5
3A		10
3B		10
4A		5
4B		10
4C		10 (218A only)
total		95 (145A), 115 (218A)

I certify that

- 1) I have taken no more than 3 hours to work the exam. (or DSP) limits.
- 2) I have help from no one
- 3) I did not read the exam until the time I took the exam.

Signed: _____

$$G_T = \frac{|S_{21}|^2 (1 - |\Gamma_s|^2)(1 - |\Gamma_L|^2)}{|(1 - \Gamma_s S_{11})(1 - \Gamma_L S_{22}) - S_{21} S_{12} \Gamma_s \Gamma_L|^2} \quad G_p = \frac{1}{1 - \|\Gamma_{in}\|^2} \cdot |S_{21}|^2 \cdot \frac{1 - |\Gamma_L|^2}{|1 - \Gamma_L S_{22}|^2}$$

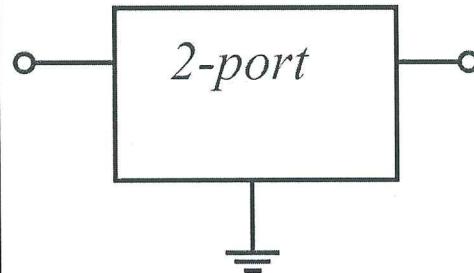
$$G_a = \frac{1 - |\Gamma_s|^2}{|1 - \Gamma_s S_{11}|^2} \cdot |S_{21}|^2 \cdot \frac{1}{1 - \|\Gamma_{out}\|^2} \quad G_{\max} = \frac{|S_{21}|}{|S_{12}|} \cdot [K - \sqrt{K^2 - 1}] \text{ if } K > 1$$

$$G_{MS} = \frac{|S_{21}|}{|S_{12}|} \cdot \text{if } K < 1 \quad K = \frac{1 - |S_{11}|^2 - |S_{22}|^2 + |\Delta|^2}{2 |S_{21} S_{12}|} \quad \text{where } \Delta = \det[S]$$

Unconditionally stable if : (1) $K > 1$ and (2) $\|\det[S]\| < 1$

Problem 1, 35 points (145A), 45 points (218A)
Two-port properties, Power gain definitions

At a signal frequency of 10 GHz, a transistor has $S_{11} = 0$, $S_{12} = 0.2$, $S_{21} = 2$ and $S_{22} = 0.8$, as defined with a 50 Ohm impedance reference.



part a, 5 points

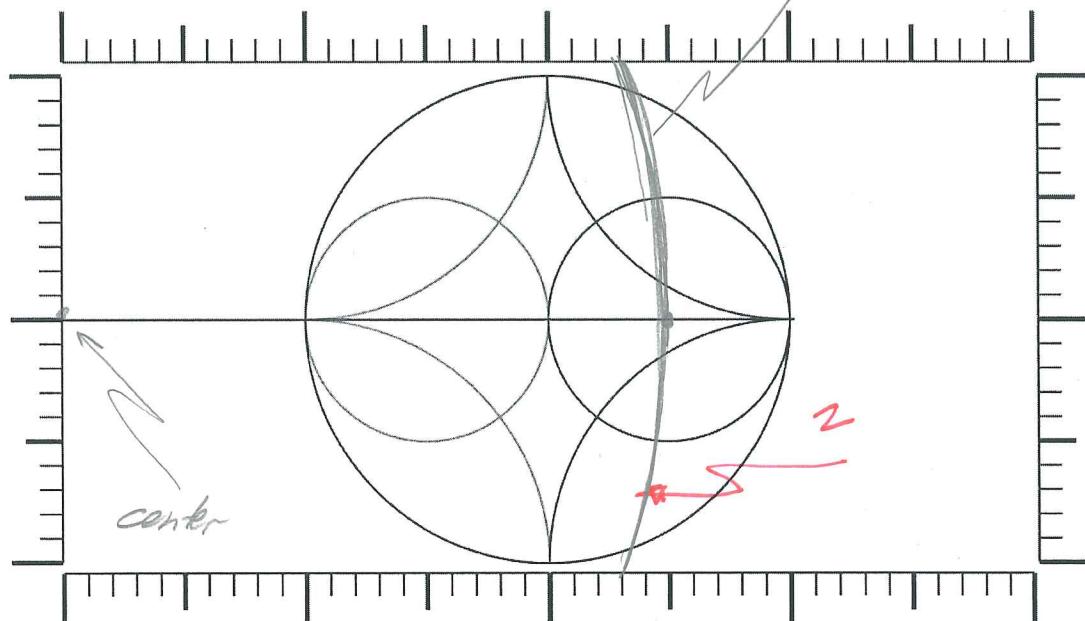
Is the circuit unconditionally stable? Hint: examine the expressions for Γ_{in} and Γ_{out}

Yes/No: ? No

$$\begin{aligned}
 2 & \quad \boxed{\Gamma_{out} = S_{22} + \frac{S_{12} S_{21} \Gamma_s}{1 - S_{11} \Gamma_s}} \\
 2 & \quad \boxed{= 0.8 + \frac{(0.2)(2) \Gamma_s}{1 - (0) \Gamma_s} = 0.8 + 0.4 \Gamma_s} \\
 1 & \quad \boxed{\text{clearly if } \Gamma_s = 1, \Gamma_{out} = 1.2} \\
 & \quad \text{so the 2-port is potentially unstable.}
 \end{aligned}$$

part b, 5 points

Please draw a SOURCE stability circle



Circle of radius = 2.5
centred at $\beta_s = -2$
crosses axis at $\beta_s = -4.5$.

| [Source stability circles are values of β_s
which give $|P_{out}| = 1$

| [If $|P_{out}| = 1$ then $P_{out} = e^{j\theta}$

| [$P_{out} = e^{j\theta} = 0.8 + j0.4 \beta_s$

$$0.4 \beta_s = e^{j\theta} - 0.8$$

| [$\beta_s = 2.5 \cdot e^{-j\theta} - 2$

Circle of radius = 2.5

centred at $\beta_s = -2$

4

θ	$e^{-j\theta}$	β_s
0	1	0.5
-180°	-1	-4.5

part c, 5 points

The transistor is connected to a 100 Ohm generator with 1mW available power. The output of the transistor is impedance matched to the load. How much power will be delivered to the load ?

load power = 1.9mW

$\boxed{I_S = \frac{100/50 - 1}{100/50 + 1} = \frac{1}{3}}$

$$S_{11} = 0 \quad S_{22} = 0.2$$

$$S_{21} = 2 \quad S_{12} = 0.8$$

this is the available gain

$\boxed{G_a = \frac{1 - I_S^2}{1 - (I_S S_{11})^2} \cdot \frac{1}{1 - (I_S S_{11})^2} \cdot |S_{21}|^2}$

$\boxed{P_{out} = S_{22} + \frac{S_{21} S_{12} I_S^2}{1 - S_{11} I_S}}$
 $= 0.8 + 0.4 I_S^2 = 0.8 + 0.4 \left(\frac{1}{3}\right)^2 = 0.933\bar{3}$

$\boxed{G_a = \frac{1 - I_S^2}{1 - (I_S \cdot 0)^2} \cdot \frac{0.8}{1 - (0.933\bar{3})^2} = \frac{0.888\bar{8}}{1} \cdot \frac{4}{1.871} = 0.90}$

$P_{out} = G_a \cdot P_{av} = 1.9 \text{ mW}$

part d, 10 points

IF necessary, the transistor is stabilized, and is then impedance-matched to a 50 Ohm generator and a 50 Ohm load ? If the generator has 1mW available power, how much power will be delivered to the load ?

load power = 10mW.

1 [- transfer is polarity unstable
2 [- so after stabilization, the generator will be the master

1 [- $MSE = \frac{|S_{21}|}{|S_{12}|} = \frac{2}{0.2} = 10$

1 [$P_{load} = 10 \cdot 1mW = 10mW$

part e, 10points

In the case of part D, above, if we consider the transistor, the possible stabilization network, and the matching networks to be, in combination, an amplifier, what are $\|S_{21}\|$, $\|S_{11}\|$, $\|S_{22}\|$, $\|S_{12}\|$ of the *amplifier*?

$$\|S_{11}\| = \underline{\hspace{2cm} 0 }$$

$$\|S_{12}\| = \underline{\hspace{2cm} 0.316 }$$

$$\|S_{21}\| = \underline{\hspace{2cm} 3.16 }$$

$$\|S_{22}\| = \underline{\hspace{2cm} 0.0 }$$

2 [- the amplifier has been matched
so $|S_{11}^T| = |S_{22}^T| = 0$

4 [- the amplifier ^{power} gain is $|S_{21}|^2$ and is equal
to the K_{ST} m₈₆ = 10
 $|S_{21}|^2 = 10 \rightarrow |S_{21}^T| = 3.16$

4 [- and (tricky) the matching and stab./bz. netwks
are reciprocal, so

$$\frac{|S_{21}^T|}{|S_{12}^T|} = \frac{|S_{21}|^2}{|S_{12}|^2} = \frac{2}{0.2} = 10$$

$$\Rightarrow |S_{12}^T| = \frac{|S_{21}^T|}{10} = \underline{\hspace{2cm} 0.316 }$$

part f, 10 points (somewhat tricky, ece218c students only)

I now connect the *amplifier* of parts D and E to a 100 Ohm generator of 1 mW available power. The load is 25 Ohms. What is the power delivered to the load ?

Pload=_____

Sorry, I have messed up.
Can't be solved!

$$G_T = \frac{I_{S_2} I^2 (1 - |I_S|^2) (1 - |I_L|^2)}{|(1 - S_{21} I_S)(1 - S_{22} I_L) - S_{12} S_{21} I_S I_L|^2}$$

$$S_{11} \approx S_{22} = 0 \quad |S_{21}| = 3.16 \quad |S_{12}| / |S_{21}| = 0.316$$

$$G_T = \frac{(S_{21})^2 (1 - |I_S|^2) (1 - |I_L|^2)}{|1 - S_{12} S_{21} I_S I_L|^2}$$

Unfortunately, though we know $|S_{21}|, |I_S|, |I_L|$

and though we know $|S_{21} S_{12}|$, we don't
know $S_{12} S_{21}$!

*10 pT₃
for nothing
that
we can't solve*

Cannot answer (f).

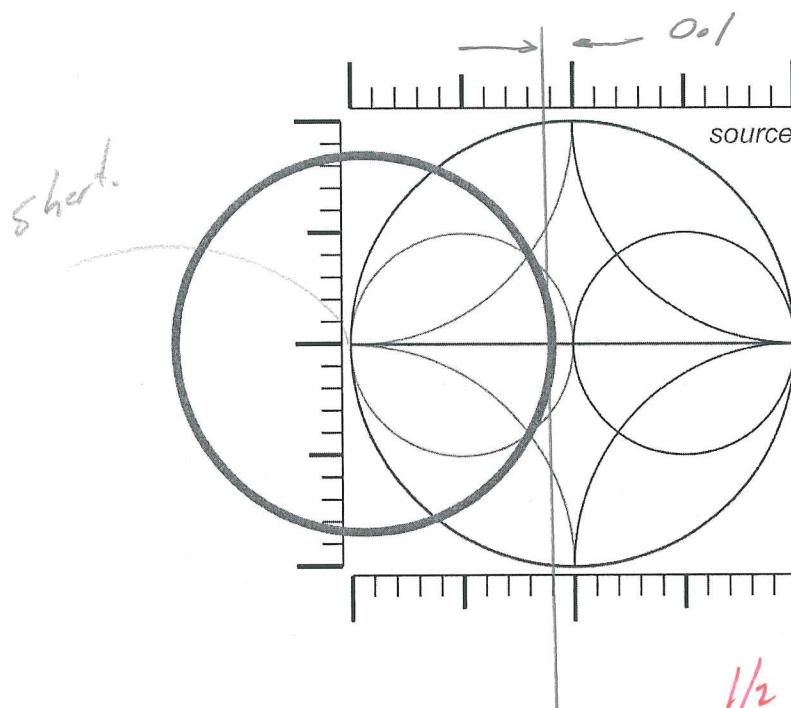
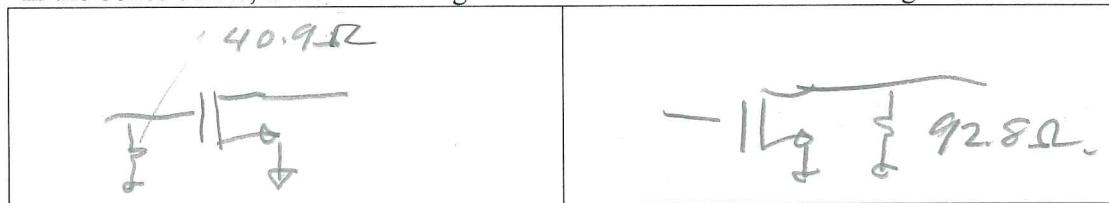
Problem 2, 25 points

Unconditionally stable and Potentially unstable amplifier design, gain circles

part a, 10 points

A MOSFET in common-source mode has $\|S_{11}\| < 1$ and $|S_{22}| > 1$, and has stability circles (50Ω reference impedance) as below.

In the boxes below, draw circuit diagrams of *two* methods of stabilizing the transistor.

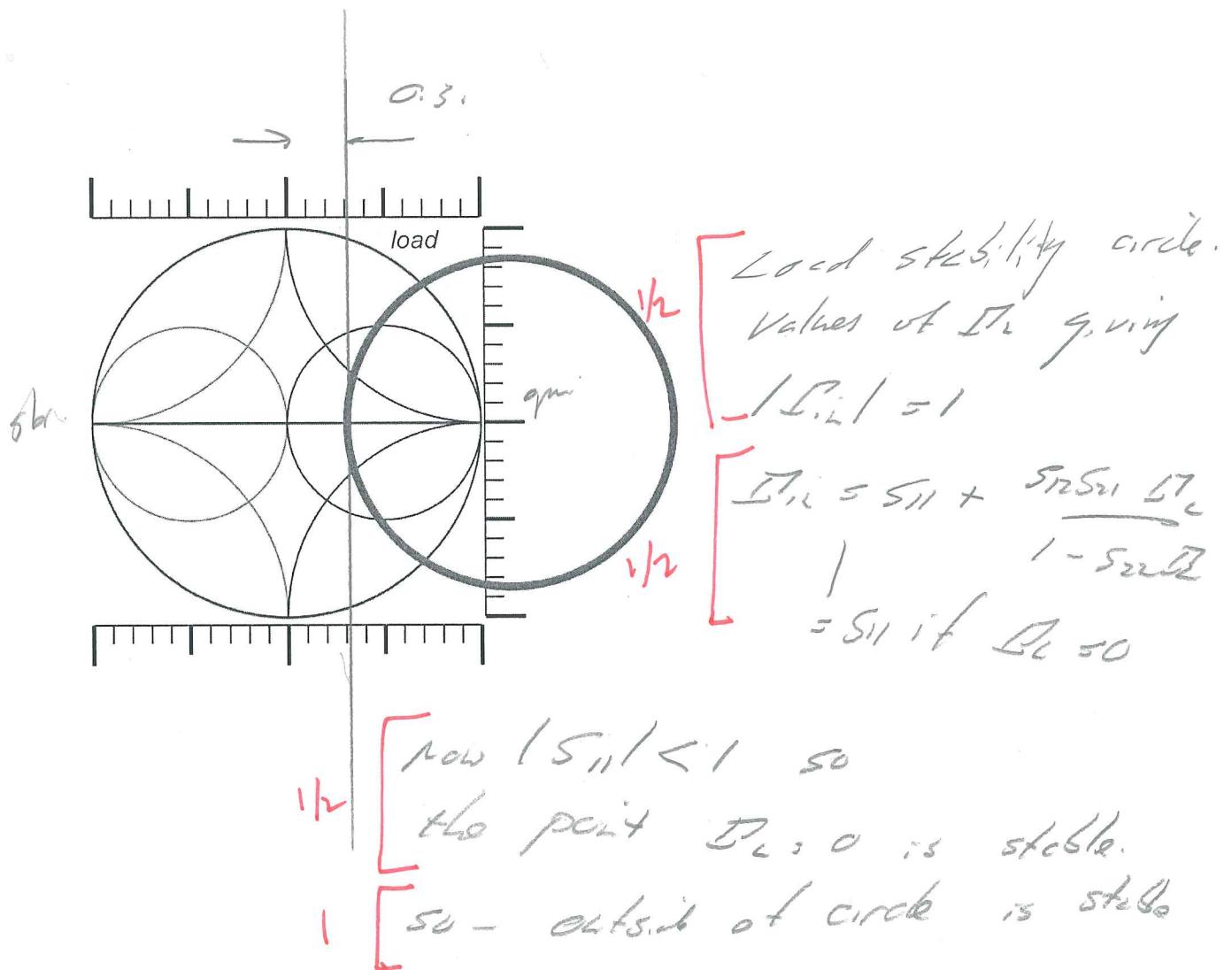


$$\boxed{1/2} \begin{aligned} &\text{source stability circle:} \\ &\text{values of } \beta_3 \text{ giving} \\ &|T_{21}| = 1 \end{aligned}$$

$$\boxed{1/2} \begin{aligned} P_{out} &= S_{22} - \frac{S_{12} S_{21} S_3}{1 - S_{11} S_3} \\ &= S_{22} \text{ if } \beta_3 = \infty \end{aligned}$$

$$\boxed{1/2} \begin{aligned} &\text{Now } |S_{22}| > 1 \text{ so the} \\ &\text{point } (\beta_3 = 0) \text{ is unstable} \end{aligned}$$

$$\boxed{1} \begin{aligned} &\underline{\underline{\Sigma^0 - \cancel{\text{inside}} \text{ of C. side is stable}}} \end{aligned}$$



source

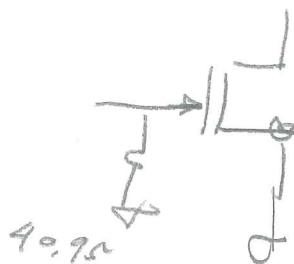
I [stability circle is tangent to $D = -0.1$

$$\Rightarrow R = 50\Omega \cdot \frac{1-0.1}{1+0.1} = 50\Omega \cdot \frac{9}{11} = 40.9\Omega.$$

~~connect to~~

we can stabilize by adding a shunt ω_n of

↓



Load

I [stability circle is tangent to $D_{30,3}$

$$R = 50\Omega \cdot \frac{1+0.3}{1-0.3} = 50\Omega \cdot \frac{13}{7} = 92.8\Omega$$

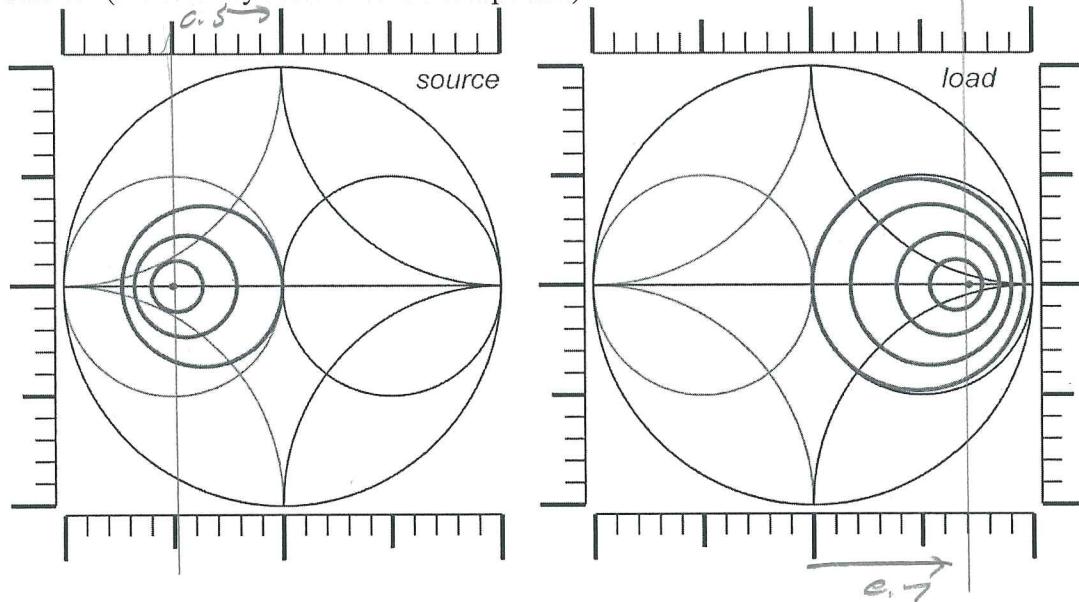
[we need to keep Z_L less than this, so
we need a shunt connection

I



part b, 5 points

A transistor has available and operating gain circles as below. These are graphed in 1dB gain increments. The transistor MAG is 10dB, and the transistor is unconditionally stable. (the stability circles have been updated)



What source and load impedances are required for 10dB gain?

$$Z_s = \underline{16.7\Omega} \quad Z_l = \underline{283.3\Omega}$$

2⁵

$$\boxed{Z_s = 50\Omega \cdot \frac{1+0.5}{1+0.1} = 50\Omega \cdot \frac{1}{3} = 16.7\Omega}$$

2⁵

$$\boxed{Z_l = 50\Omega \cdot \frac{1+0.7}{1-0.7} = 50\Omega \cdot \frac{17}{3} = 283.3\Omega}$$

part c, 5 points

We continue with the transistor of part B. If the generator impedance is 50Ohms, and the load impedance is matched to the transistor output impedance, then what power gain will the transistor have?

Power gain = 7dB

3 [A 50Ω generator is 3 steps (circles)
away from Z_{opt.} → 3dB gain decrease
7dB gain] 2

part d, 5 points

We continue with the transistor of part B. If the load impedance is 50Ohms, and the generator impedance is matched to the transistor input impedance, then what power gain will the transistor have ?

Power gain = 6dB

P_L
3 [50Ω load is 4 octaves away
from Z_{in} → 4dB gain decrease]₂

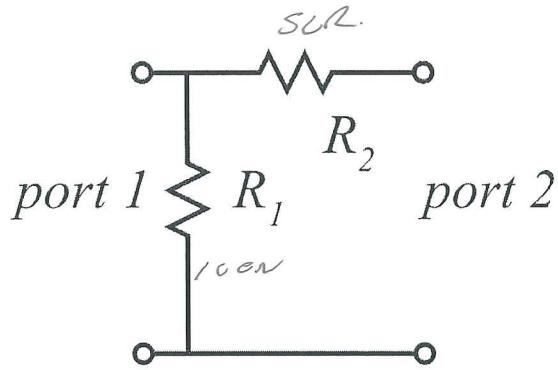
Problem 3, 20 points

S parameters and Signal flow graphs

part a, 10 points

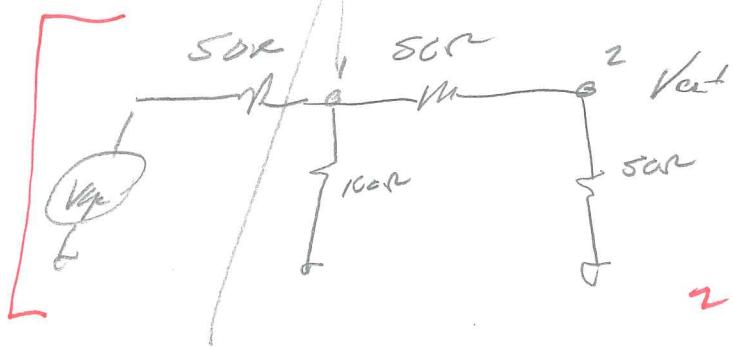
We have $R_1=100$ Ohms and $R_2=50$ Ohms

Given a 50 Ohm impedance reference,
Find the four S-parameters of this network



$$S_{11} = 0, \quad S_{12} = \frac{1}{2}, \quad S_{21} = \frac{1}{2}, \quad S_{22} = 1/4.$$

1 [$Z_L = 100\Omega // (100\Omega) = 50\Omega$]



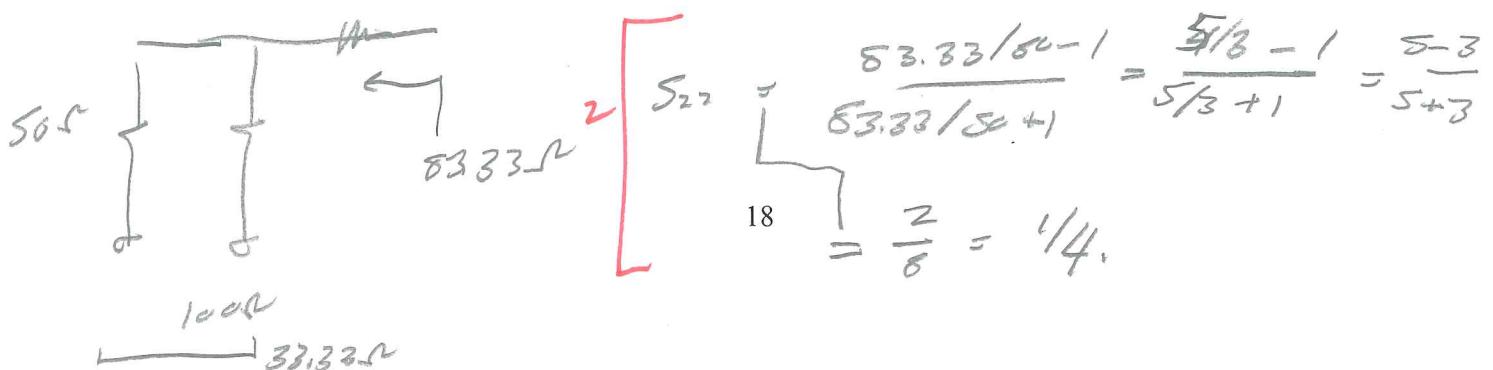
2 [$\frac{V_{out}}{V_{in}} = \frac{50\Omega}{50\Omega + 50\Omega} \frac{Z_L}{Z_L + 50\Omega}$]

$$= \frac{1}{2} \cdot \frac{1}{2} = 1/4$$

2 [$S_{21} = 2 \frac{V_o}{V_{in}} / (Z_L = Z_S - Z_0) = 1/2$]

1 [Since $Z_L/Z_{in} = 50\Omega$, $S_{11} = 0$.]

$S_{11} = S_{22}$ reciprocity.] 2
 $= 1/2$



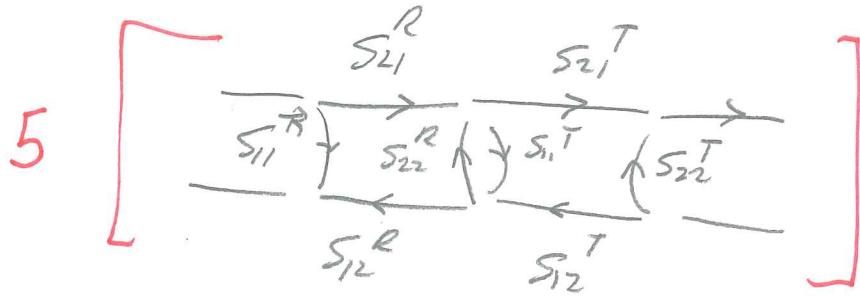
2 [$S_{22} = \frac{83.33/80-1}{83.33/80+1} = \frac{\sqrt{18}-1}{\sqrt{18}+1} = \frac{5-3}{5+3}$]

$$= \frac{2}{8} = 1/4.$$

part b, 10 points

We have a transistor having $S_{11}=0.5$, $S_{12}=0.1$, $S_{21}=3$, $S_{22}=0.5$. We connect the network of part a to the transistor's INPUT. Please find S_{21} of the combined network

S_{21} of the combined network = 1.714



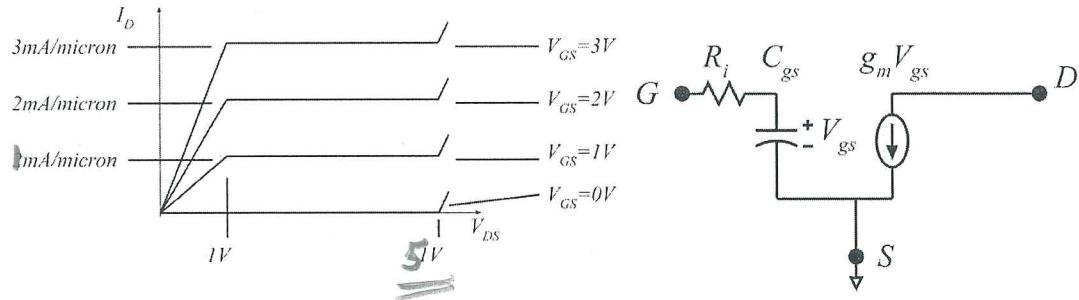
4

$S_{21, \text{combined}} = \frac{S_{21}^R S_{21}^T}{1 - S_{22}^R S_{11}^T}$

$= \frac{(1/2)(3)}{1 - (1/4)(1/2)} = \frac{3/2}{1 - 1/8} = \frac{3/2}{7/8}$

$= \frac{3 \cdot 4}{7} = \frac{12}{7} = 1.714$

Problem 4, 15 points (145A), 25 points (218A)
Power amplifier design (the graph is updated)



A FET has the common-source characteristics above, normalize to FET gate width. You can infer g_m from the data above. We have $R_i = 1/g_m$, and f_t is 200 GHz. The signal frequency is 50 GHz

part a, 5 points

Assuming TEMPORARILY a 1 micron FET width, give the following.

$$g_m = 1 \text{ mS} \quad C_{gs} = 0.795 \text{ fF} \quad R_i = 1 \text{ k}\Omega$$

2
$$g_m = \frac{3 \text{ mA/micron}}{3V} = \frac{3 \text{ mA}}{3V} = 1 \text{ mS}$$

2
$$f_t = \frac{1}{2\pi C_{gs}} \rightarrow G_s = \frac{1}{2\pi f_t} = 0.795 \text{ fF}$$

1
$$R_i = 1/g_m = 1 \text{ k}\Omega$$

$$f = 50 \text{ kHz}$$

part b, 10 points

When we attempt to design impedance-transformation networks, we have found that the minimum load impedance we can synthesize is 10 Ohms.

What FET width should we select ? $W_g = 133.3 \mu\text{m}$

What maximum undistorted output power will we obtain ? $P_{out} = 200 \text{ mW}$

What input power does this require ? $P_{in} = 7.38 \text{ mW}$

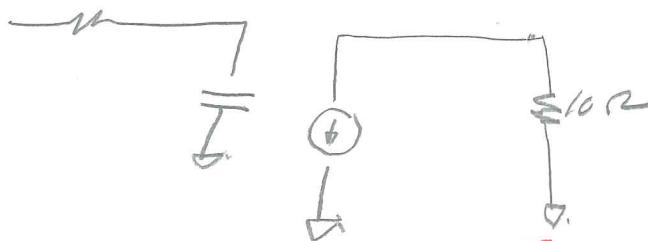
$$1 \quad [SV - IV = 4V \text{ swing}]$$

$$1 \quad [R_L = 10\Omega \rightarrow I_{max} = \frac{4V}{10\Omega} = 400 \text{ mA.}]$$

$$1 \quad [\text{max current} = 3mA/\mu\text{m} \rightarrow \text{width} = \frac{400 \text{ mA}}{3mA/\mu\text{m}} \\ = 133.3 \mu\text{m}]$$

$$2 \quad [IV = 4V. \quad \Delta I = 400 \text{ mA.}]$$

$$2 \quad [P_{max} = \frac{1}{8} \cdot IV \cdot \Delta I = \frac{4V \cdot 400 \text{ mA}}{8} = 200 \text{ mW}]$$



$$1 \quad g_d = 1 \mu\text{s}/\mu\text{m} \cdot 133 \mu\text{m} \\ = 133 \text{ mS}$$

$$1 \quad R_s = 1/g_m = 7.5 \Omega$$

$$1 \quad C_{gs} = \frac{g_m}{2\pi f_L} = 0.1057 \text{ pF}$$

$$P_{out} = I_{out}^2 \cdot R_L$$

$$I_{in} = I_{out} \cdot \frac{2\pi f G_s}{R_a} = I_{out} \cdot \frac{f}{\frac{R_a}{2\pi f G_s}}$$

3

$$P_{in} = I_{in}^2 \cdot R_i = I_{out}^2 \left(\frac{f}{\frac{R_a}{2\pi f G_s}} \right)^2 \cdot R_i$$

$$\frac{P_{out}}{P_{in}} = \frac{R_L}{R_i} \cdot \left(\frac{f_{in}}{f} \right)^2 = \frac{10\Omega}{7.5\Omega} \left(\frac{2000\text{Hz}}{500\text{Hz}} \right)^2$$

$$= \frac{4}{3} (4)^2 = \frac{4^3}{3}$$

1

$$P_{in} = \frac{P_{out}}{\frac{4^3}{3}} = \frac{200\text{mW}}{4^2/3} = \frac{200\text{mW}}{21.3} = 9.38\text{mW}$$

2