

High linearity GaN HEMT power amplifier with pre-linearization gate diode

Shouxuan Xie, Vamsi Paidi, Sten Heikman, Alessandro Chini,
Umesh K. Mishra, Mark J. W. Rodwell and Stephen I. Long

Department of ECE, UC Santa Barbara, CA 93106, USA

A high linearity MMIC RF power amplifier is reported in the AlGaIn/GaN HEMT technology. In order to obtain high linearity, a pre-linearization gate diode is added at the input to compensate for the nonlinear input capacitance C_{gs} of the GaN HEMT device. Another single-ended Class B power amplifier without the gate diode is also designed for comparison. The circuit with the pre-linearization gate diode demonstrates at least 4dB improvement on 3rd order intermodulation distortion (IMD3) performance over the one without the diode up to its P_{1dB} output power level in two-tone measurement.

1. Introduction.

Our previously described single-ended Class B power amplifier design using GaN HEMT technology has shown that high linearity and high efficiency can be obtained simultaneously if the circuit is biased at exactly the pinch off point (Class B configuration) [1].

In order to further improve the linearity performance, linearization techniques, such as feedback, feed forward, or predistortion, could be used. Feedback is good for audio power amplifiers, but for RF power amplifiers the stability and efficiency can be compromised. System-level feed forward or predistortion can be useful for RF frequencies, but the overall efficiency will be decreased because extra components have to be added. This will also make it difficult for MMIC implementation.

In this work, three major sources of AlGaIn/GaN HEMT device nonlinearity are investigated: nonlinear I-V characteristic (transfer function), nonlinear G_{ds} and nonlinear C_{gs} . All the above nonlinear sources in the device will create the 3rd order intermodulation distortion (IMD3) either at the output or at the input of the circuit.

2. Nonlinear sources of GaN HEMT device.

The nonlinear I-V characteristic of the device will generate intermodulation products when two or more signals are present. It is the dominant nonlinear source when the input power exceeds the compression point. But at moderate input power level, as we discussed in [2], best linearity performance can be obtained by biasing at Class B.

For GaN HEMT devices, the output conductance G_{ds} is a nonlinear function of the input and output signal. Moreover, substantial drain voltage dependent leakage current is observed in 0.3 μ m single gate devices, which is similar to the Drain-Induced Barrier Lowering (DIBL) effect in MOSFETs. This results in a threshold voltage shift, which also creates IMD3. However, by using dual-gate device, the nonlinear G_{ds} effect can be minimized. The upper device reduces the drain voltage variation on the lower device and decreases the output conductance.

Finally, the input capacitance C_{gs} vs. V_{gs} characteristic of the AlGaIn/GaN HEMT device is not linear, and it can be proved that the nonlinear C_{gs} characteristic can generate intermodulation distortion at the input of the circuit [3]. The distorted signal is amplified by the active device, also

affecting the linearity of the circuit. The effect due to nonlinear C_{gs} can be compensated by adding a pre-linearization gate diode [4].

3. Pre-linearization gate diode

The nonlinear C_{gs} vs. V_{gs} characteristic can be modeled as a hyperbolic tangent function, which is an anti-symmetric function around the center point, V_c (shown in figure 1). Therefore, the idea of further improving linearity due to nonlinear C_{gs} is to compensate it with another nonlinear capacitance. Suppose C_{gs} vs. V_{gs} relation is modeled as $C_{gs} = C_1 \cdot \tan H(V_{gs} - V_c) + C_0$, shown in figure 2, an ideal nonlinear capacitor $C_{pd} = -C_1 \cdot \tan H(V_c - V_{gs}) + C_0$ can be added in parallel with C_{gs} such that the total input capacitance of the circuit is constant (not a function of bias voltage V_{gs}). This idea can be easily implemented by adding another HEMT device at the input, whose source and drain are shorted, and the gate is biased at twice of the V_c (shown in figure 3).

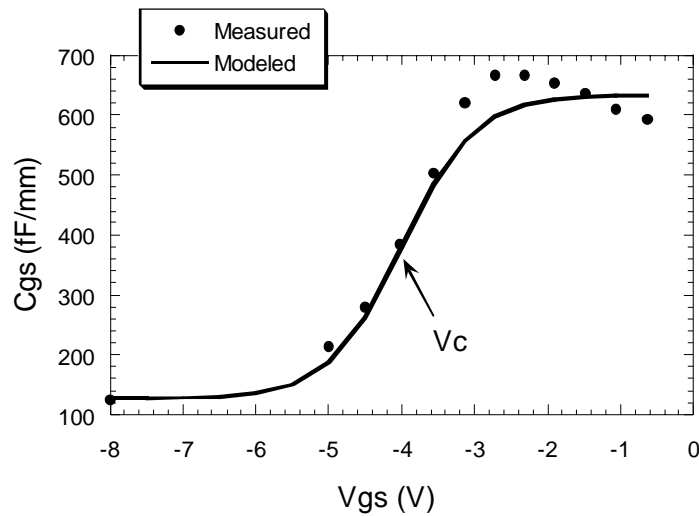


Figure 1: Nonlinear C_{gs} vs. V_{gs} characteristic of AlGaN/GaN HEMT

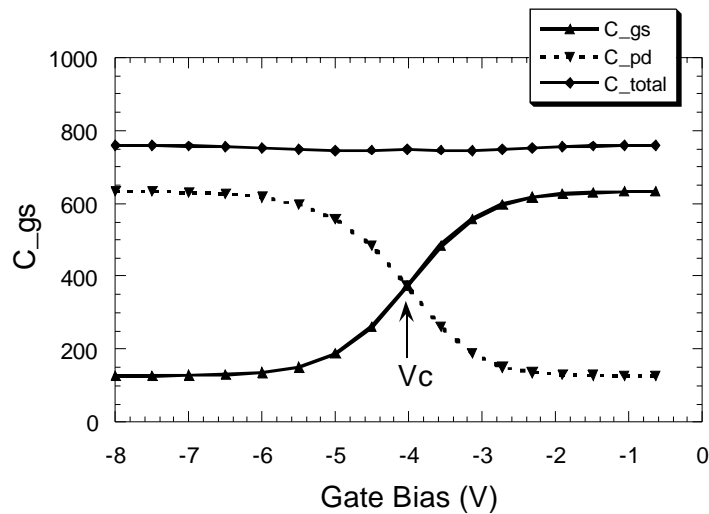


Figure 2: Pre-linearization gate diode to further improve linearity due to nonlinear C_{gs}

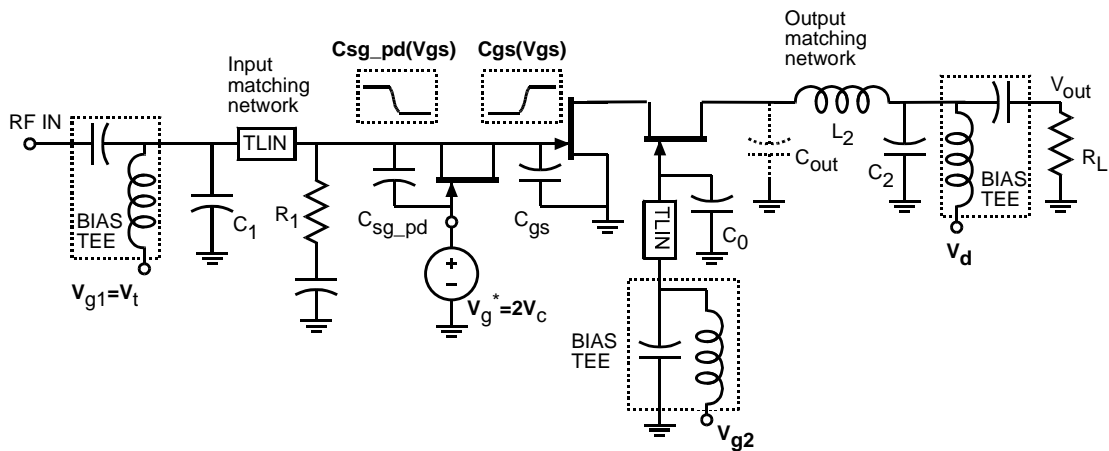


Figure 3: Circuit schematic of the dual-gate Class B PA with the pre-linearization diode

4. Circuit design and simulation result.

The dual gate Class B power amplifier with the prelinearization gate diode is designed at 10GHz. The Curtice cubic model (C_FET3) is used to model the GaN HEMTs device and the circuit is simulated using Agilent ADS. The circuit schematic and IC layout are shown in Figures 3 and 4. Both the input and output matching networks are on chip. In the output matching network, the output capacitance of the device C_{out} is absorbed into the Pi low pass filter. A 10pF on-chip capacitor C_0 is needed as close to the second gate as possible to provide a good AC ground at the frequency of interest. The prelinearization gate diode is beside the main device, both of which have the same gate area. Another single-ended Class B power amplifier without the gate diode is also designed for comparison. According to simulation using a model which only includes nonlinear C_{gs} , the IMD3 performance can be improved by about 4dB with the pre-linearization gate diode at all power levels (shown in Figure 5).

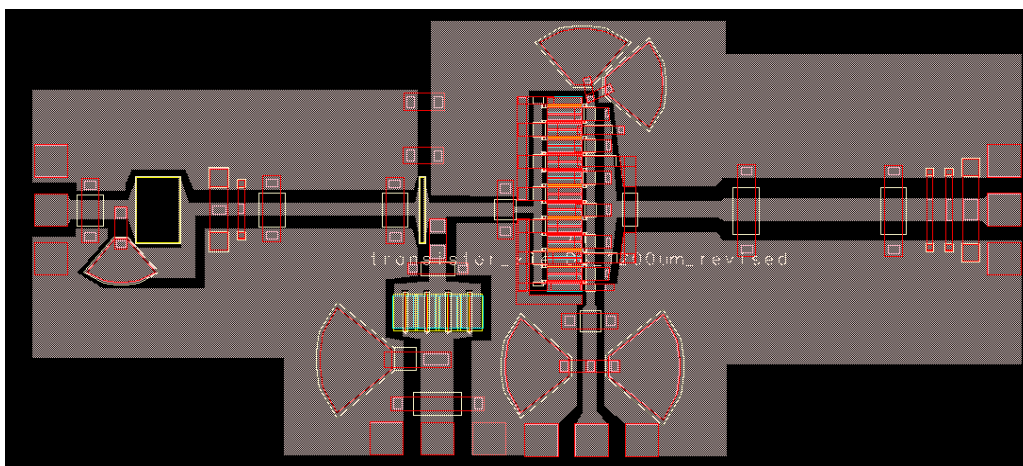


Figure 4: IC layout (dimensions 3mm x 1.5mm)

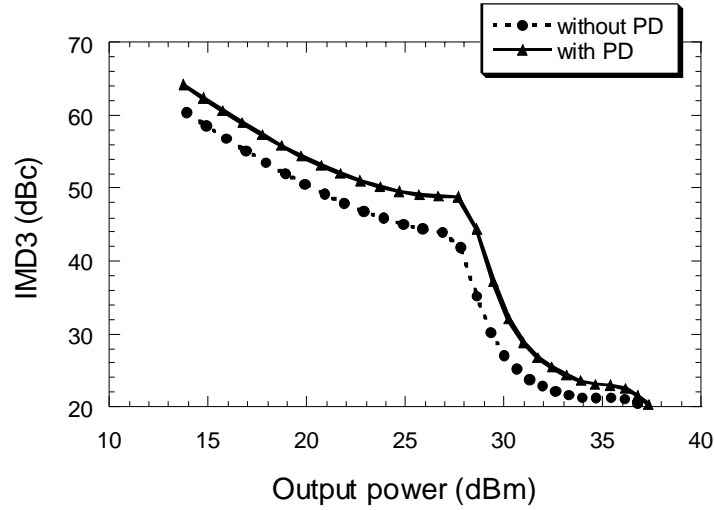


Figure 5: Simulation result on IMD3 improvement with the pre-linearization diode

5. Measurement results.

All the MMIC Class B power amplifiers are fabricated on a SiC substrate in the standard AlGaIn/GaN HEMT technology [5] [6] and then tested. The 1.2mm dual gate GaN HEMT gives 1A/mm of I_{dss} and a breakdown voltage of approximately 55V. The measured f_t for the 0.3 μ m L_g dual gate device is 50GHz.

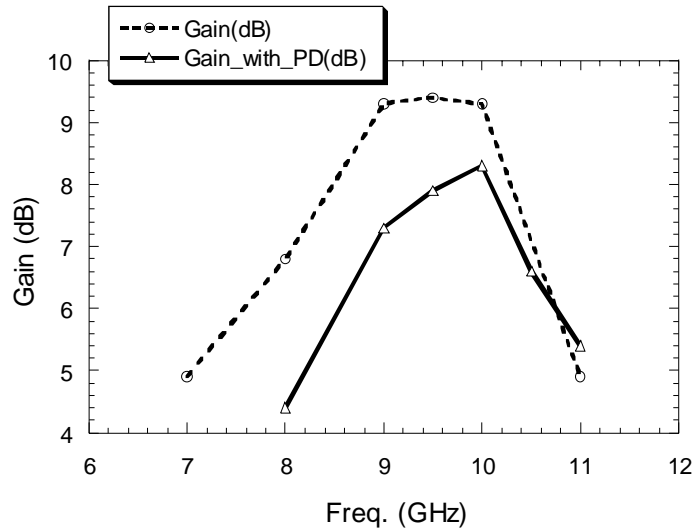


Figure 6: Measurement result: Gain vs. Freq.

The single tone and two tone measurement results of the two circuits are shown in figures 6, 7 and 8. For single tone measurement, the circuit without the gate diode demonstrates 30dBm

output power at 1dB gain compression point (P_{1dB}) with 9.5dB gain from 9GHz to 10GHz, while the circuit with the gate diode demonstrates 29.1dBm of P_{1dB} output power with 8.4 dB gain at 10GHz. For two-tone measurement around 10GHz, a peak of 51dBc IMD3 is obtained with the gate diode MMIC at around 21dBm per tone output power. A 4dB improvement in IMD3 is obtained for the whole power range up to this point.

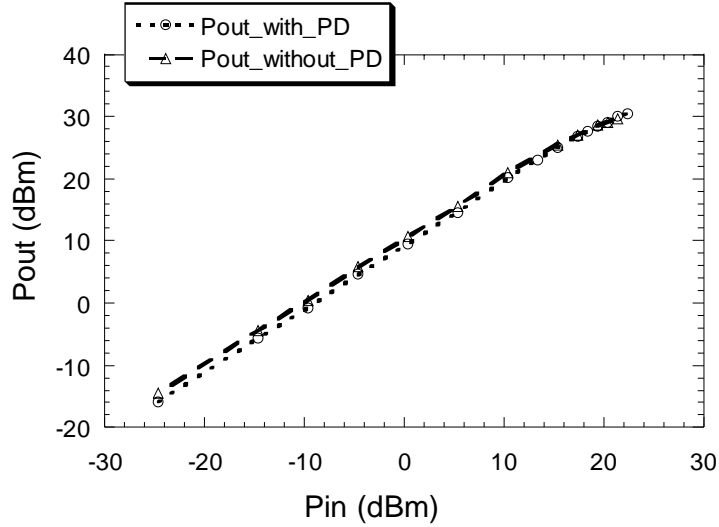


Figure 7: Single tone measurement result Pout vs. Pin

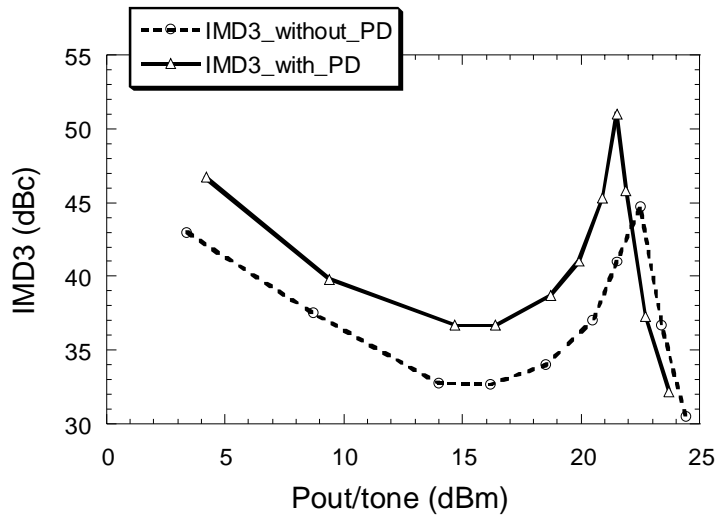


Figure 8: Two-tone measurement result IMD3 vs. Pout

6. Conclusions.

A high linearity MMIC RF power amplifier is reported in the AlGaN/GaN HEMT technology. Three major nonlinear sources of the GaN HEMT device are identified and

investigated. To further improve linearity, a pre-linearization gate diode is added at the input to compensate the nonlinear effect of Cgs. The circuit with the pre-linearization gate diode demonstrates at least 4dB improvement on IMD3 performance over the useful power range in two-tone measurement.

7. Acknowledgement.

This work is supported by the Office of Naval Research under N00014-00-1-0653. And thanks to Dr. Walter Curtice, who provided us the Curtice C_FET3 model for the simulation.

8. References

- [1] Shouxuan Xie, *et al.*, "High linearity of Class B Power Amplifiers in GaN HEMT technology." *Microwave and Wireless Components Letters*, July 2003
- [2] Vamsi Paidi, *et al.*, "High Linearity and High Efficiency of Class B Power Amplifiers in GaN HEMT Technology." *IEEE Transactions on Microwave Theory and Techniques*, Vol. 51, No. 2, Feb. 2003
- [3] Vuolevi, J., Rahkonen, T., Manninen, J, "Measurement technique for characterizing memory effects in RF power amplifiers." *Microwave Theory and Techniques, IEEE Transactions on*, Volume: 49, Issue: 8, Aug. 2001 Pages: 1383-1389
- [4] Chengzhou Wang; Larson, L.E.; Asbeck, P.M., "A nonlinear capacitance cancellation technique and its application to a CMOS class AB power amplifier." *Radio Frequency Integrated Circuits (RFIC) Symposium, 2001. Digest of Papers. IEEE*, May 2001
- [5] Ching-Hui Chen; Coffie, R.; Krishnamurthy, K.; Keller, S.; Rodwell, M.; Mishra, U.K. "Dual-gate AlGaIn/GaN modulation-doped field-effect transistors with cut-off frequencies $f_T > 60$ GHz"; *Electron Device Letters, IEEE*, Volume:21, Issue:12, Dec. 2000
- [6] B.M. Green, *et al.*, "High-power broad-band AlGaIn/GaN HEMT MMICs on SiC substrates," *IEEE Transactions on Microwave Theory and Techniques*, vol. 49, no. 12, Dec. 2001