

ECE 2C Homework #6 Due: Wednesday 11/25/09 at 5 PM

Relevant readings:

S&S Section 6.8, 6.11, 7.1-7.3, 7.5-7.6,

8 problems

- (1) [10 pt.] S&S Problem 6.100 {Frequency response of NMOS cascode amplifier}
- (2) [10 pt.] S&S Problem 6.107 (c) {CMOS folded-cascode amplifier}
- (3) [10 pt.] S&S Problem 6.122 {BJT CC-CE pair}
- (4) [10 pt.] S&S Problem 7.17 {CMOS differential amplifier with resistive load}
- (5) [10 pt.] S&S Problem 7.13 {CMOS differential amplifier with active load}
- (6) [10 pt.] S&S Problem 7.21 {BJT differential amplifier basic operation}
- (7) [10 pt.] S&S Problem 7.34 { Basic operation of BJT differential amplifier with emitter degeneration resistors}
- (8) [10 pt.] S&S Problem 7.85 {CMOS differential amplifier frequency response}