

University of California, Santa Barbara  
Dept. of Electrical and Computer Engineering  
ECE 15B – Computer Organization  
Homework #2 – SPIM + Basic Assembly

**SPIM (45 points - 3 points each question)**

The following questions are based on FIR\_Filter.txt file

([http://www.ece.ucsb.edu/~kastner/ece15b/homework/FIR\\_Filter.s](http://www.ece.ucsb.edu/~kastner/ece15b/homework/FIR_Filter.s)). Start by loading this file then answer the following questions:

1. Which of the windows is the register? Memory? Instruction?
2. What's the address of the first instruction in your code (addi \$sp, \$sp, -4)?
3. What's the content of registers a0, a1, a2 when the instruction "jal FIR" is executed? (For extra credit, explain why)
4. What is the beginning address of in\_array? csnt\_array? (Hint: the instruction *la* takes two arguments; the first is the destination register for the address from the second argument.)
5. What's the value of ra AFTER the jal FIR instruction is executed? (For extra credit, explain why)

Set a breakpoint at instruction 0x00400088 and run the program until you hit that breakpoint:

6. To which instruction does this breakpoint correspond?

7. What is the value of t0?

8. What is the value of t4?

9. Why is t3 significantly larger than t0?

10. What's the content of the address that t3 points to?

Set another breakpoint at instruction 0x004000b0 and run the program until you hit this breakpoint:

11. What's the value of t3? t5?

12. What's the value that t3 is pointing to?

Run through 5 breakpoints (NOTE: first encounter after you resume is the same one as you set before so do not count it!)

13. Which breakpoint did you encounter first? 0x00400088 or 0x004000B0? Why?

14. How many breakpoints will it hit before it hits the 0x00400088 breakpoint? Why?

15. After 5 breakpoints, what's the value of t3? t5?



17. (25 points) Translate the following code into MIPS assembly:

$$A[i] = B[i-1] + B[i] + B[i+1]$$

Assume that  $A$  and  $B$  are byte arrays. Both arrays are stored in memory. Register \$1 contains the initial address for array  $A$ ; register \$2 contains the initial address for array  $B$ ; and register \$3 contains the value of  $i$ .

Assume that load (lw) and store (sw) take 3 cycles each.

How many cycles does your code take? How many additional registers does your code require?