ECE202A Final Exam.

This is a 3-hour exam. There are 3 questions. Please don't turn the cover page until the exam is distributed to everyone.

Use any and all reasonable approximations in circuit analysis, after stating them.

Name: ____________________________
Problem 1, 50 points:
Reactive-matched amplifiers, Stability, etc

Part A, 10 points
Properties of S-parameters

Find the 4 S-parameters for a the transistor at Left in a 50Ω system at 10 GHz. The transistor model is shown below. The transistor has a 100 mS transconductance, Ri is zero, Rds is 500Ω, and Cgs is 0.318 pF.
An amplifier \( S_a \) has \( S_{a11} = 0.2, S_{a22} = 0.3, S_{a21} = 5 \) \( S_{a12} = 0.1 \). 50\( \Omega \) resistors are connected to ground on the input and output, thus. Find \( S_b^{21} \) of the overall network.
The stability circles for a bipolar transistor is shown to the left. $|S_{11}|=2$, $|S_{22}|=0.5$. The system impedance is $50\Omega$.

Show circuit diagrams for two methods of ensuring that the transistor is stable, together with the values of the components involved.
Part D, 10 points
*Power gain definitions, impedance matching*

Ga and Gp circles for the transistor are shown at left (10 GHz). The center of the Ga circles is $\Gamma=+0.5$, and the center of the Gp circles is $\Gamma=-0.25$.

Using the Smith chart, find the values of the components required to Match the amplifier to a 50Ω system at 5 GHz.
An transistor $(S')$ has $S'_{11}=0.5$, $S'_{22}=0.25$

$S'_{21}=10$ $S'_{12}=0$, given a 50 ohm impedance

definition

The generator is 25 ohms, the load is 75 ohms.

Impedance-matching networks are connected to the amplifier input and output, creating an amplifier A.

1) Find the MAGNITUDES of the 4 S-parameters of the amplifier A
2) Find the transducer power gain of the amplifier given that the generator is 25 ohms and the load is 75 ohms.
3) Find the transducer power gain of the amplifier if connected to a 50Ω generator and load.
On the right are a SiC MESFET’s output characteristics. \( V_{\text{knee}}=5\, \text{V} \), \( V_{\text{br}}=105\, \text{V} \), \( I_{\text{dss}}=200\, \text{mA per millimeter of Gate Width} \).

**A: 5 points:** We want to design a 1 kW class-a power amplifier. What Gate Width is required?

**B: 5 points:** What is the load impedance required for this?

**C: 5 points:** What would be the DC-to-RF efficiency?

**D: 5 points:** If the load impedance was increased 30% from this value, what would be the maximum output power?
Problem 3, 30 points:  
Design Problem

The transistor at left has 1000 mS/mm transconductance, \( R_i = 1/g_m \), a 160 GHz current gain cutoff frequency and a 250 GHz power gain cutoff frequency.

The parameters above are obtained with \( V_{ds} = 2 \) V and \( V_{gs} = -0.2 \) Volts.

It is desired to have an amplifier with 15 dB gain and 100 GHz bandwidth at the 3-dB-point. You will use resistive feedback amplifiers, probably several cascaded stages. Design such a amplifier, showing your calculations for all relevant parameters, and give a full circuit diagram.