Demonstration (35 points total)

- (10) Demo program correctness
- (5) Control unit and decode designed efficiently
- (10) Data path pipelined correctly and efficiently
- (5) Timing methodology
- (5) FPGA understanding (appendix C or control implementation)

Non–pipelined speed: ______
Pipelined Speed: ______

Experiment Competence (5 points total, separate grade for each member)

- (5) Group member 1: Name:______________________________
- (5) Group member 2: Name:______________________________
- (5) Group member 3: Name:______________________________

Lab Report (50 points total)

- (3) Abstract
- (20) Hardware Description
  - Data path
  - Control unit
  - Basic building blocks
  - Functional block diagram – non–pipelined
  - Functional block diagram – pipelined
  - Schematic diagram – pipelined
  - Timing diagram
- (10) Experiment Application
  - Description of operations
  - Program flow
  - Source code in RTL
- (10) Discussion
  - Timing considerations
  - Pipelining
  - Performance
- (3) English/grammar and report organization
- (4) Conclusion

Demo T.A. ______________________________
Report T.A.______________________________