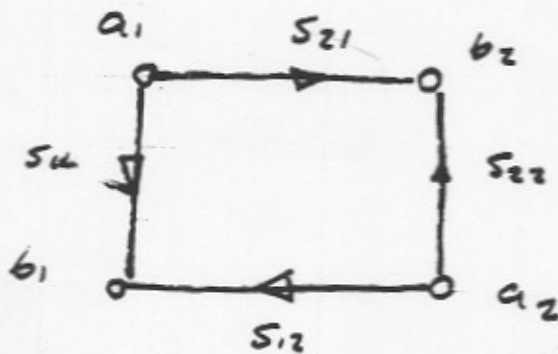


Signal Flow Graphs.

Mason (Control system theory)

$$b_1 = s_{11} a_1 + s_{12} a_2$$

$$b_2 = s_{21} a_1 + s_{22} a_2$$

represent as below:

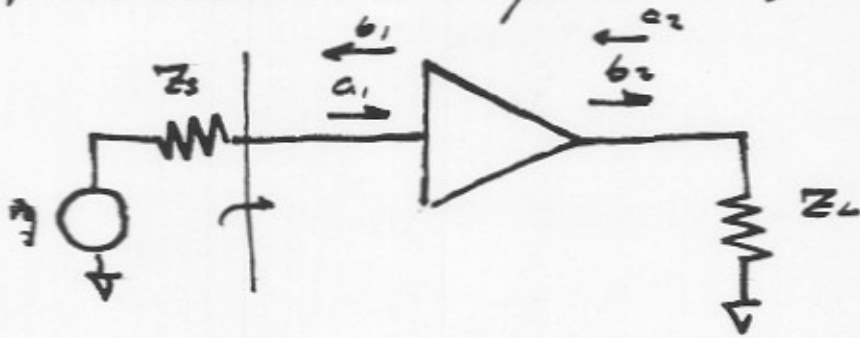
variables denoted as nodes

$$\begin{matrix} \circ \\ \text{---} \\ \circ \end{matrix} a_1$$

value of variable = sum of entering branches

= sum of values of connecting nodes times weights of branches.

Representation of generator, load



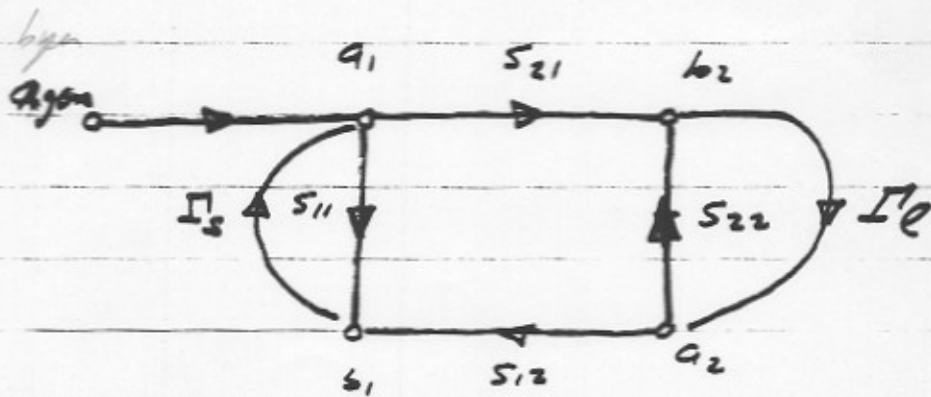
and

$$V^+ = V_g \Gamma_s + \Gamma_L V^-$$

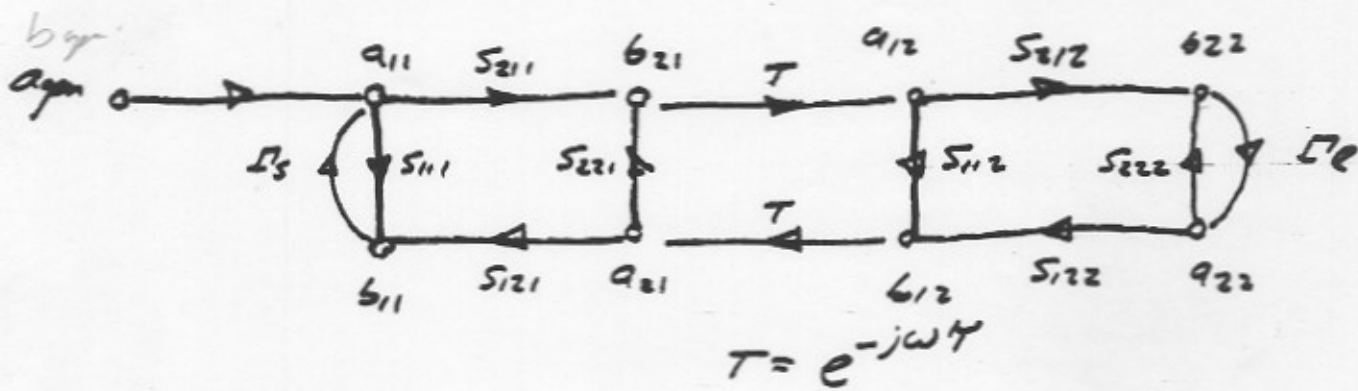
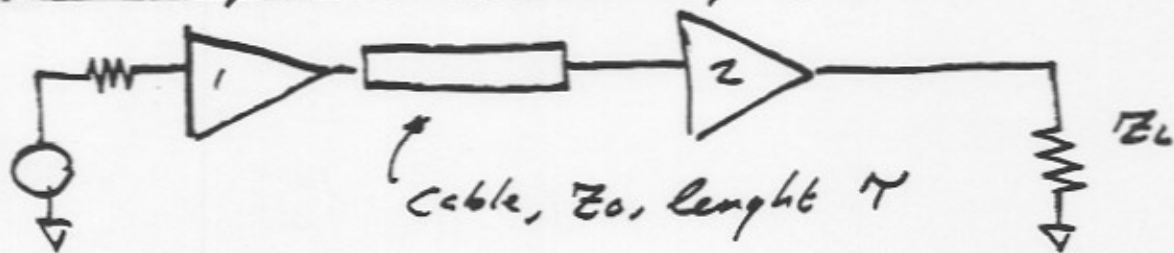
$$V^+ / \sqrt{Z_0} = V_g \Gamma_s / \sqrt{Z_0} + \Gamma_L \frac{V^-}{\sqrt{Z_0}}$$

$$a_1 = \Gamma_{gen} + \Gamma_L b_1$$

$$a_2 = \Gamma_L b_2$$



Second example: Cascaded amplifiers



represents
 The above relates the many equations of the system.

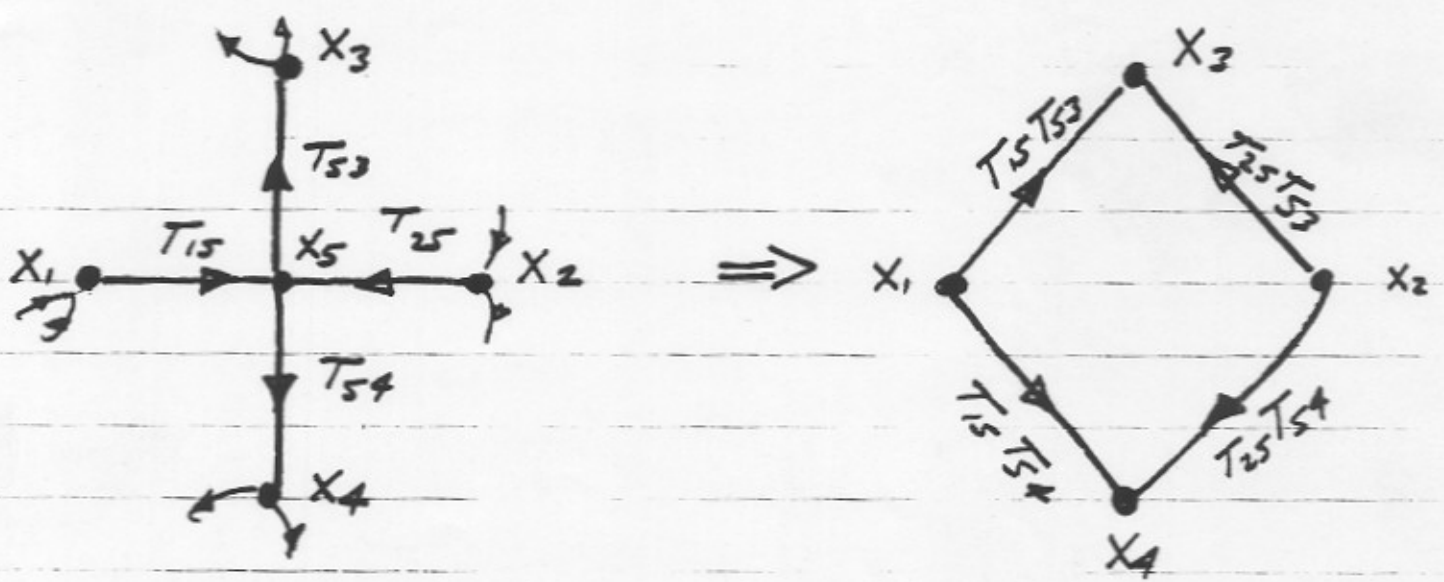
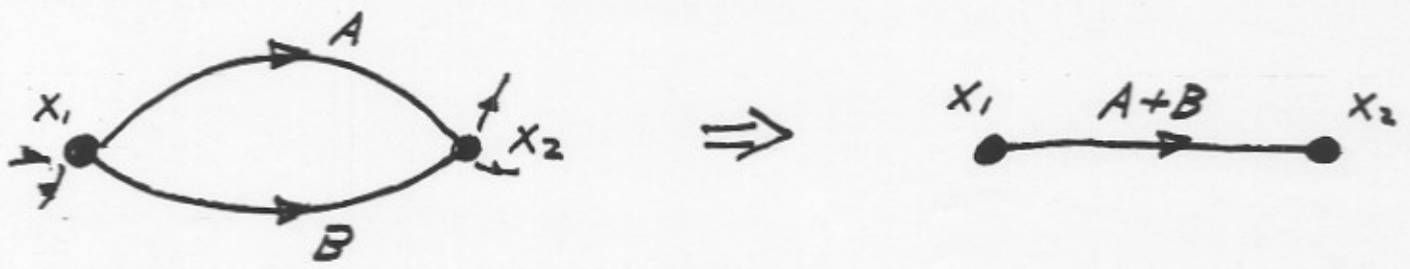
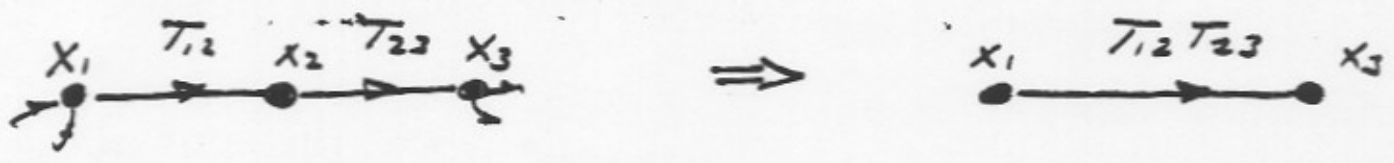
Signal Flow graphs most heavily used in control system theory, where they

- 1) Provide an organized way of presenting a set of mathematical relationships.
- 2) Lend some visual intuition to aid in solving otherwise "dry" simultaneous equations.
(Kind of like Macintosh vs P.C.)
- 3) Provide efficient solution through "Mason's gain rules"

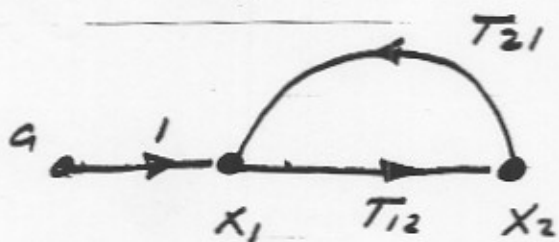
Ref: S.J. Mason "Feedback theory - some properties of Signal Flow Graphs" Proc. IRE, 41, p. 1141, Sept 1953.

Ref Any text on general control system theory.

Important flow-graph Manipulations



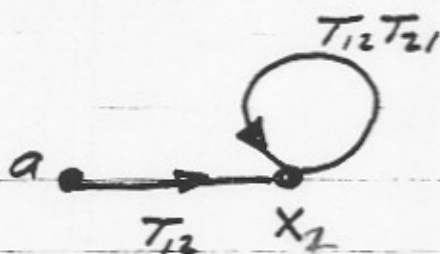
Reducing a Feedback loop



$$x_2 = T_{12} x_1$$

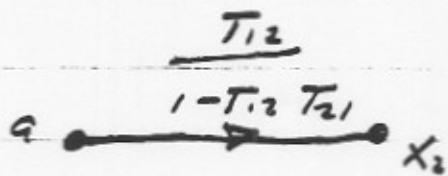
$$x_1 = a + T_{21} x_2$$

$$\Rightarrow x_2 = T_{12} a + T_{12} T_{21} x_2$$



\Leftarrow simpler representation

$$\Rightarrow x_2 = \frac{T_{12}}{1 - T_{12} T_{21}} a$$




\Leftarrow simpler still.

Mason's Gain Rule



- define $T = b/a$ "transmission"

- define a path P_i as any route following the direction of the arrows taking you from input to output without going through any node twice.

- define a loop  as the product (ABC) of the transmission coefficients around any loop.

$$T = \frac{b}{a} = \frac{P_1 [1 - \sum L(1)^{(1)} + \sum L(2)^{(1)} - \dots] + P_2 [1 - \sum L(1)^{(1)} + \dots]}{1 - \sum L(1) + \sum L(2) - \sum L(3)}$$

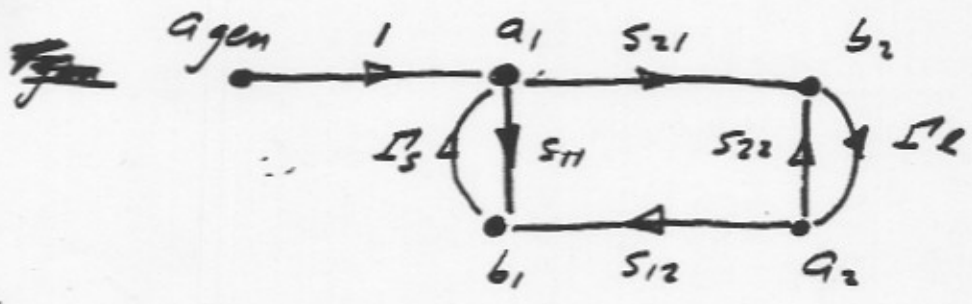
here $\sum L(1)^{(1)}$ means sum of all loops.

$\sum L(1)^{(1)}$ means sum of all loops which don't touch path P_1 .

$\sum L(2)$ = sum of all second-order loops
~~number~~ second-order loops = product of
 any two nontouching loops.

similarly $L(3)$, $L(2)^{(1)}$, etc.

Back to simple amplifier



find $b_2 = T$
 $\overline{a_{gen}}$

$$T = \frac{S_{21} \left[1 - \sum L^{(1)} + \sum L^{(2)} - \dots \right]}{1 - \underbrace{\Gamma_s S_{11} - \Gamma_L S_{22}}_{\sum L^{(1)}} + (\Gamma_s S_{11}) (\Gamma_L S_{22}) - S_{21} \Gamma_L S_{12} \Gamma_s}$$

no second path
 ↓

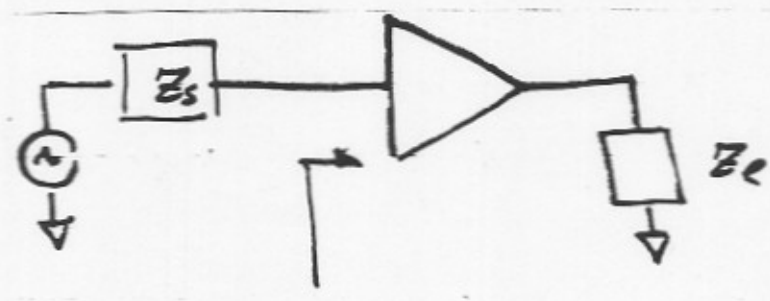
$$= \frac{S_{21}}{1 - \Gamma_s S_{11} - \Gamma_L S_{22} + (\Gamma_s S_{11})(\Gamma_L S_{22}) - S_{21} \Gamma_L S_{12} \Gamma_s}$$

~~$$= \frac{S_{21}}{(1 - \Gamma_s S_{11})(1 - \Gamma_L S_{22})}$$~~

Easy, wasn't it?
 (tricks from the days before Mathematica)

well maybe: as you can see, I initially missed a loop.

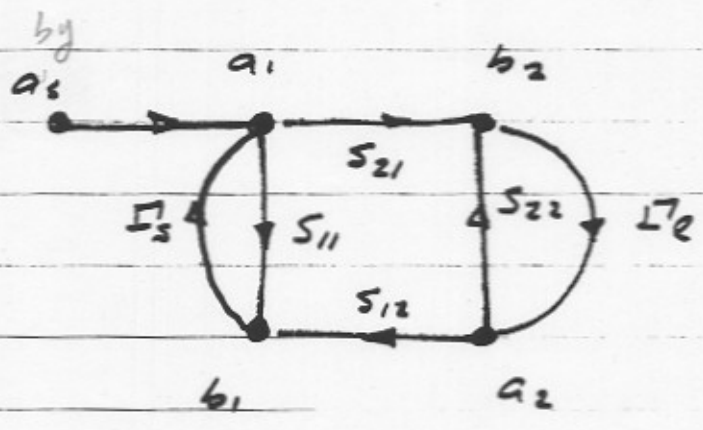
Input Reflection coefficient:



Z_{in}, Γ_{in}

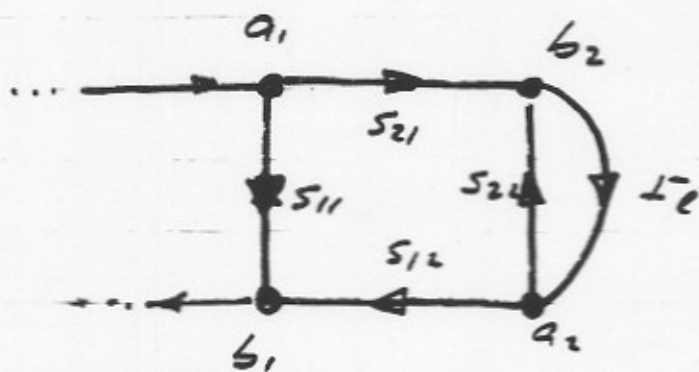
$$\Gamma_{in} = \frac{Z_{in} - Z_0}{Z_{in} + Z_0}; \quad Z_{in} = Z_0 \frac{1 + \Gamma_{in}}{1 - \Gamma_{in}}$$

$\Gamma_{in} = S_{11}$ if $Z_L = Z_0$, but not in general
 $Z_{in} = Z_0 \frac{1 + S_{11}}{1 - S_{11}}$
 (unless $S_{12} = 0$)



Block diagram

looking at the relationship between b_1 & a_1 :



$$\frac{b_1}{a_1} = \Gamma_{in} = \frac{S_{11} [1 - S_{22} \Gamma_L] + S_{21} \Gamma_L S_{12}}{1 - S_{22} \Gamma_L}$$

$$\Gamma_{in} = S_{11} + \Gamma_L \frac{S_{12} S_{21}}{1 - S_{22} \Gamma_L}$$

Input impedance { reflection coefficient } depends somewhat on
 load { reflection coefficient } unless $S_{12} S_{21} = 0$
 { impedance }

Similarly:

$$\Gamma_{out} = S_{22} + \Gamma_S \frac{S_{12} S_{21}}{1 - S_{11} \Gamma_S}$$

If $S_{12}S_{21} = 0$ then either $S_{12} = 0$ or $S_{21} = 0$.

In other words the amplifier can't pass signals both ways (input \rightarrow output, output \rightarrow input).

Such an amplifier is unilateral.

If an amplifier is unilateral then $\Gamma_{in} = S_{11}$ and $\Gamma_{out} = S_{22}$. Matching on input & output is easy.

If the amplifier is bilateral ($S_{12}S_{21} \neq 0$) (passes signals both ways) then

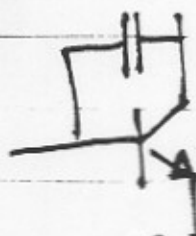
Input $\left\{ \begin{array}{l} \text{impedance} \\ \text{reflection coeff.} \end{array} \right\}$ depends on load

$\left\{ \begin{array}{l} \text{impedance} \\ \text{reflection coeff.} \end{array} \right\}$; and vice-versa.

This is a problem: design an output matching network, and you have changed the load impedance on the device, hence the input impedance also changes, disturbing the input match.

Process of matching becomes iterative in the presence of nonzeros S_{12} , S_{21} . In some cases, it is not possible to match on both input & output.

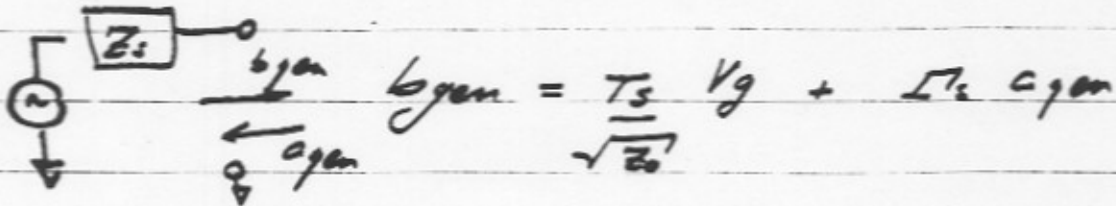
S_{12} arises mostly from Egd Cbc



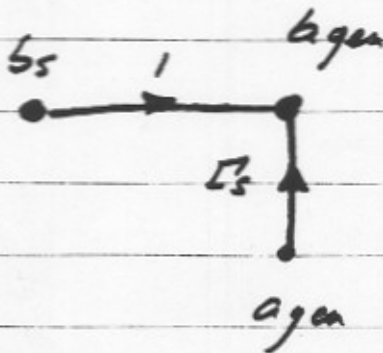
Rf in the feedback amplifier also introduces small S_{12} . Be aware of this in adding matching to lab #3.

Available source power from a different viewpoint

$$P_{avg} = \frac{\|V_{open\ circuit}\|^2}{4 \operatorname{Re}\{Z_s\}} \quad \text{R.M.S.}$$

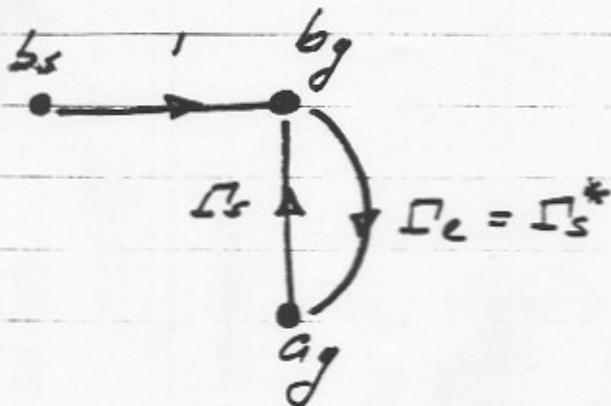


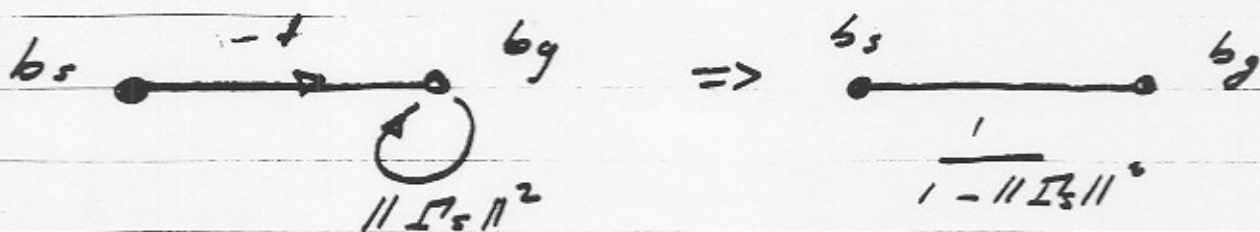
$$b_{gen} = b_s + \Gamma_s a_{gen}$$



b_s is the wave amplitude launched into $Z_L = Z_0$

Now connect conjugate-matched load Z_s^* ($\Rightarrow \Gamma_s^*$)





$$\text{reverse power} = |b_s|^2 \frac{\|\Gamma_s\|^2}{[1 - \|\Gamma_s\|^2]^2} = \frac{\|\Gamma_s\|^2 |b_s|^2}{[1 - \|\Gamma_s\|^2]^2}$$

$$\text{forward power} = \frac{|b_s|^2}{[1 - \|\Gamma_s\|^2]^2}$$

$$\text{load power} = \text{available power} = \frac{|b_s|^2}{1 - \|\Gamma_s\|^2}$$

$$P_{\text{avail}} = \frac{|b_s|^2}{1 - \|\Gamma_s\|^2} \leftarrow \text{Power launched into } Z_0$$