

# ***ECE122 - VLSI Principles***

University of California, Santa Barbara

Department of Electrical and Computer Engineering

Fall 2014

**Class Room:** ESB 1003 (Cooper Lab) Tue & Thu 3:30PM-4:45PM

**Labs:** Tuesdays 8:00 AM-10:50 AM HFH 1140 (ECI lab)

**Instructor:** [Prof. Kaustav Banerjee](#), Room 4151, Harold Frank Hall  
Email: [kaustav@ece.ucsb.edu](mailto:kaustav@ece.ucsb.edu)

**Office Hours:** Fri 1:00PM-2:00PM or appointment by email.

**TA:** Junkai Jiang (head TA) [junkaijiang@ece.ucsb.edu](mailto:junkaijiang@ece.ucsb.edu)

**Office Hours:** Tues 11:00AM-12:30PM or appointment by email.

**Text Book:** *CMOS VLSI Design: A Circuits and Systems Perspective* (4<sup>th</sup> Edition),  
Neil H. E. Weste and David Harris, Addison Wesley, © 2011.

**Supplementary Text:** *Modern Semiconductor Devices for Integrated Circuits* (First  
Edition), Chenming Hu, Prentice Hall, © 2010.

**References:** To be posted on the class homepage:

**Prerequisites:** ECE152A (Digital Design Principles) or Equivalent  
Elementary Level Semiconductor Device Physics (Desirable)

<b>Grading:</b>	Homework (including lab)	20%
	Midterm	20%
	Final Project	20%
	Final Exam	40%

Note:

- Late homework will be penalized (20% per day).
- Must complete **all labs** and **final project** to pass.