

Final Exam Info: Topics Covered

- KVL/KCL
- Parallel/series equivalent resistances
- Voltage/current division
- Nodal analysis
- Mesh analysis
- Source transformations
- Thevenin and Norton equivalents
- Power, maximum power transfer
- Superposition
- Gain (voltage and current gain)
- Op-amp basics (inverting amp, non-inverting amp, summing amp, difference amp, buffer stage, comparator)
- Capacitors and inductors
- RC, RL, LC, and RLC circuits:
 - Natural response
 - Initial conditions
 - Setting up ODEs
 - Response to sinusoidal waveform
- Phasors and complex impedances